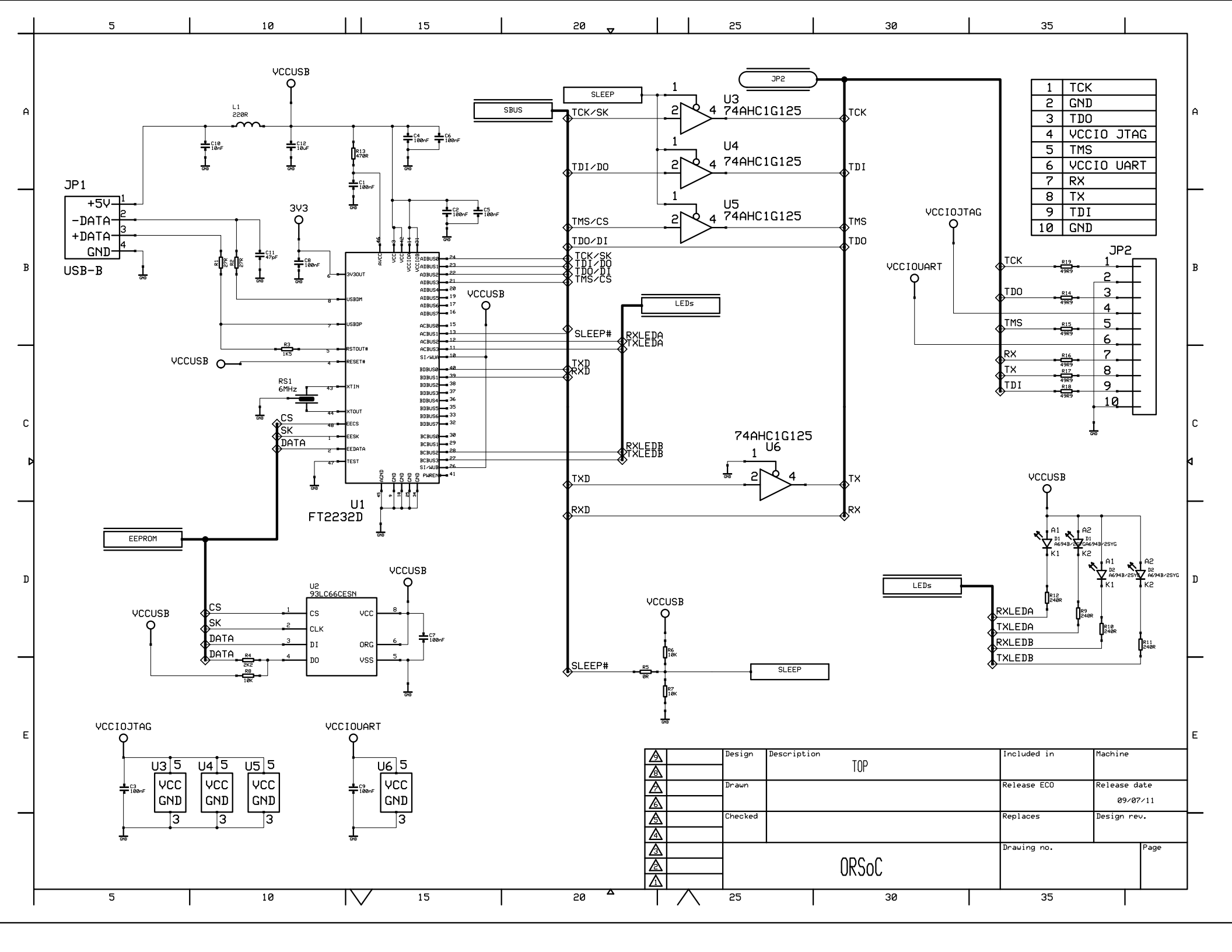
JP1
+5V
-DATA
+DATA
GND
USB-B
VCCUSB
3V3
L1
220R
U1
FT2232D
EEPROM
U2
93LC66CESN
CS
SK
DATA
SBUS
SLEEP
TCK/SK
TDI/DO
TMS/CS
TDO/DI
SLEEP#
TXD
RXD
U3
74AHC1G125
U4
74AHC1G125
U5
74AHC1G125
U6
74AHC1G125
JP2
TCK
TDI
TMS
TDO
TX
RX
LEDs
RXLEDA
TXLEDA
RXLEDB
TXLEDB
VCCIOJTAG
VCCIOUART
1 TCK
2 GND
3 TDO
4 VCCIO JTAG
5 TMS
6 VCCIO UART
7 RX
8 TX
9 TDI
10 GND
ORSoC
TOP
Design
Drawn
Checked
Description
Included in
Machine
Release ECO
Release date
09/07/11
Replaces
Design rev.
Drawing no.
Page