

INPUT	OUTPUT
M0F0	CPX
M0F1	CPY
M1F0	CPZ
M1F1	DPO
M2F0	DP1
M2F1	
M3F0	
M3F1	
M4F0	
M4F1	
M5F0	
M5F1	

OPCODE

STAGE 0		STAGE 1		STAGE 2	
S0_M0F0	S0_P0	S1_P0	S1_A0	S2_A0	S2_A3
S0_M0F1		S1_P1			
S0_M1F0	S0_P1	S1_P2	S1_A1	S2_P2	S2_A3
S0_M1F1					
S0_M2F0	S0_P2	S1_P3	S1_A2	S2_P3	S2_A4
S0_M2F1					
S0_M3F0	S0_P3	S1_P4	S1_A2	S2_P3	S2_A4
S0_M3F1					
S0_M4F0	S0_P4	S1_P5	S1_A2	S2_P3	S2_A4
S0_M4F1					
S0_M5F0	S0_P5	S1_P5	S1_A2	S2_P3	S2_A4
S0_M5F1					

S0_OPCODE

S1_OPCODE

