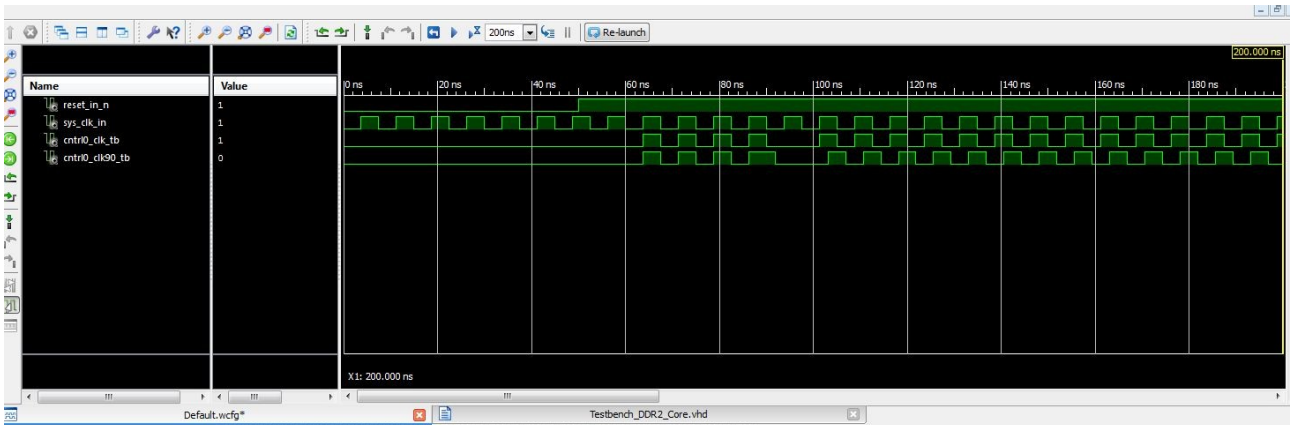


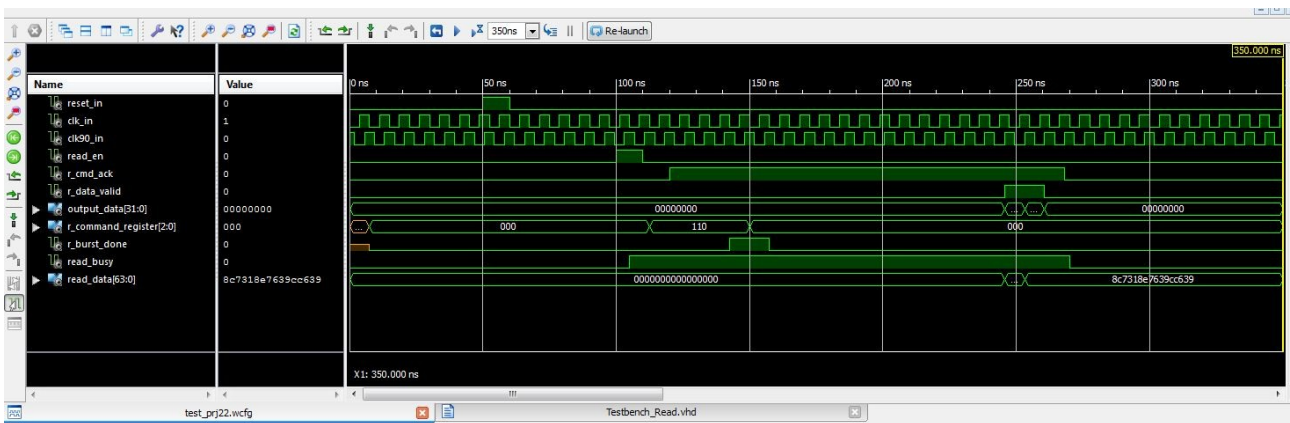
Clock-Generation (DDR2_Ram_Core.vhd)

Testbench_DDR2_Core.vhd



Read-Timing (DDR2_Read_VHDL.vhd)

Testbench_Read.vhd



Write-Timing (DDR2_Write_VHDL.vhd)

Testbench_Write.vhd

