

Code			Mnemonic of Z80/Z180/eZ80 CPUs				
DEC	HEX	OCT	w/o prefix	DD nn	CB nn	FD CB dd nn	ED nn
0	00	000	NOP		RLC B		IN0 B, (n)
1	01	001	LD BC, nn		RLC C		OUT0 (n), B
2	02	002	LD (BC), A		RLC D		LEA BC, IX+d
3	03	003	INC BC		RLC E		LEA BC, IY+d
4	04	004	INC B		RLC H		TST B
5	05	005	DEC B		RLC L		
6	06	006	LD B, n		RLC (HL)	RLC (IY+d)	
7	07	007	RLCA	LD BC, (IX+d)	RLC A		LD BC, (HL)
8	08	010	EX AF, AF'		RRC B		IN0 C, (n)
9	09	011	ADD HL, BC	ADD IX, BC	RRC C		OUT0 (n), C
10	0A	012	LD A, (BC)		RRC D		
11	0B	013	DEC BC		RRC E		
12	0C	014	INC C		RRC H		TST C
13	0D	015	DEC C		RRC L		
14	0E	016	LD C, n		RRC (HL)	RRC (IY+d)	
15	0F	017	RRCA	LD (IX+d), BC	RRC A		LD (HL), BC
16	10	020	DJNZ \$+d		RL B		IN0 D, (n)
17	11	021	LD DE, nn		RL C		OUT0 (n), D
18	12	022	LD (DE), A		RL D		LEA DE, IX+d
19	13	023	INC DE		RL E		LEA DE, IY+d
20	14	024	INC D		RL H		TST D
21	15	025	DEC D		RL L		
22	16	026	LD D, n		RL (HL)	RL (IY+d)	
23	17	027	RLA	LD DE, (IX+d)	RL A		LD DE, (HL)
24	18	030	JR \$+d		RR B		IN0 E, (n)
25	19	031	ADD HL, DE	ADD IX, DE	RR C		OUT0 (n), E
26	1A	032	LD A, (DE)		RR D		
27	1B	033	DEC DE		RR E		
28	1C	034	INC E		RR H		TST E
29	1D	035	DEC E		RR L		
30	1E	036	LD E, n		RR (HL)	RR (IY+d)	
31	1F	037	RRA	LD (IX+d), DE	RR A		LD (HL), DE
32	20	040	JR NZ, \$+d		SLA B		IN0 H, (n)
33	21	041	LD HL, nn	LD IX, nn	SLA C		OUT0 (n), H
34	22	042	LD (nn), HL	LD (nn), IX	SLA D		LEA HL, IX+d
35	23	043	INC HL	INC IX	SLA E		LEA HL, IY+d
36	24	044	INC H	INC IXH	SLA H		TST H
37	25	045	DEC H	DEC IXH	SLA L		
38	26	046	LD H, n	LD IXH, n	SLA (HL)	SLA (IY+d)	
39	27	047	DAA	LD HL, (IX+d)	SLA A		LD HL, (HL)
40	28	050	JR Z, \$+d		SRA B		IN0 L, (n)
41	29	051	ADD HL, HL	ADD IX, IX	SRA C		OUT0 (n), L
42	2A	052	LD HL, (nn)	LD IX, (nn)	SRA D		
43	2B	053	DEC HL	DEC IX	SRA E		
44	2C	054	INC L	INC IXL	SRA H		TST L
45	2D	055	DEC L	DEC IXL	SRA L		
46	2E	056	LD L, n	LD IXL, n	SRA (HL)	SRA (IY+d)	
47	2F	057	CPL	LD (IX+d), HL	SRA A		LD (HL), HL
48	30	060	JR NC, \$+d		SLI B		
49	31	061	LD SP, nn	LD IY, (IX+d)	SLI C		
50	32	062	LD (nn), A		SLI D		LEA IX, IX+d
51	33	063	INC SP		SLI E		LEA IY, IY+d
52	34	064	INC (HL)	INC (IX+d)	SLI H		TST (HL)
53	35	065	DEC (HL)	DEC (IX+d)	SLI L		
54	36	066	LD (HL), n	LD (IX+d), n	SLI (HL)	SLI (IY+d)	LD IY, (HL)
55	37	067	SCF	LD IX, (IX+d)	SLI A		LD IX, (HL)

DEC	HEX	OCT	w/o prefix	DD nn	CB nn	FD CB dd nn	ED nn
56	38	070	JR C, \$+d		SRL B		IN0 A, (n)
57	39	071	ADD HL, SP	ADD IX, SP	SRL C		OUT0 (n), A
58	3A	072	LD A, (nn)		SRL D		
59	3B	073	DEC SP		SRL E		
60	3C	074	INC A		SRL H		TST A
61	3D	075	DEC A		SRL L		
62	3E	076	LD A, n	LD (IX+d), IY	SRL (HL)	SRL (IY+d)	LD (HL), IY
63	3F	077	CCF	LD (IX+d), IX	SRL A		LD (HL), IX
64	40	100	LD B, B		BIT 0, B		IN B, (BC)
65	41	101	LD B, C		BIT 0, C		OUT (BC), B
66	42	102	LD B, D		BIT 0, D		SBC HL, BC
67	43	103	LD B, E		BIT 0, E		LD (nn), BC
68	44	104	LD B, H	LD B, IXH	BIT 0, H		NEG
69	45	105	LD B, L	LD B, IXL	BIT 0, L		RETN
70	46	106	LD B, (HL)	LD B, (IX+d)	BIT 0, (HL)	BIT 0, (IY+d)	IM 0
71	47	107	LD B, A		BIT 0, A		LD I, A
72	48	110	LD C, B		BIT 1, B		IN C, (BC)
73	49	111	LD C, C		BIT 1, C		OUT (BC), C
74	4A	112	LD C, D		BIT 1, D		ADC HL, BC
75	4B	113	LD C, E		BIT 1, E		LD BC, (nn)
76	4C	114	LD C, H	LD C, IXH	BIT 1, H		MLT BC
77	4D	115	LD C, L	LD C, IXL	BIT 1, L		RETI
78	4E	116	LD C, (HL)	LD C, (IX+d)	BIT 1, (HL)	BIT 1, (IY+d)	
79	4F	117	LD C, A		BIT 1, A		LD R, A
80	50	120	LD D, B		BIT 2, B		IN D, (BC)
81	51	121	LD D, C		BIT 2, C		OUT (BC), D
82	52	122	LD D, D		BIT 2, D		SBC HL, DE
83	53	123	LD D, E		BIT 2, E		LD (nn), DE
84	54	124	LD D, H	LD D, IXH	BIT 2, H		LEA IX, IY+d
85	55	125	LD D, L	LD D, IXL	BIT 2, L		LEA IY, IX+d
86	56	126	LD D, (HL)	LD D, (IX+d)	BIT 2, (HL)	BIT 2, (IY+d)	IM 1
87	57	127	LD D, A		BIT 2, A		LD A, I
88	58	130	LD E, B		BIT 3, B		IN E, (BC)
89	59	131	LD E, C		BIT 3, C		OUT (BC), E
90	5A	132	LD E, D		BIT 3, D		ADC HL, DE
91	5B	133	LD E, E		BIT 3, E		LD DE, (nn)
92	5C	134	LD E, H	LD E, IXH	BIT 3, H		MLT DE
93	5D	135	LD E, L	LD E, IXL	BIT 3, L		
94	5E	136	LD E, (HL)	LD E, (IX+d)	BIT 3, (HL)	BIT 3, (IY+d)	IM 2
95	5F	137	LD E, A		BIT 3, A		LD A, R
96	60	140	LD H, B	LD IXH, B	BIT 4, B		IN H, (BC)
97	61	141	LD H, C	LD IXH, C	BIT 4, C		OUT (BC), H
98	62	142	LD H, D	LD IXH, D	BIT 4, D		SBC HL, HL
99	63	143	LD H, E	LD IXH, E	BIT 4, E		LD (nn), HL
100	64	144	LD H, H	LD IXH, IXH	BIT 4, H		TST n
101	65	145	LD H, L	LD IXH, IXL	BIT 4, L		PEA IX+d
102	66	146	LD H, (HL)	LD H, (IX+d)	BIT 4, (HL)	BIT 4, (IY+d)	PEA IY+d
103	67	147	LD H, A	LD IXH, A	BIT 4, A		RRD
104	68	150	LD L, B	LD IXL, B	BIT 5, B		IN L, (BC)
105	69	151	LD L, C	LD IXL, C	BIT 5, C		OUT (BC), L
106	6A	152	LD L, D	LD IXL, D	BIT 5, D		ADC HL, HL
107	6B	153	LD L, E	LD IXL, E	BIT 5, E		LD HL, (nn)
108	6C	154	LD L, H	LD IXL, IXH	BIT 5, H		MLT HL
109	6D	155	LD L, L	LD IXL, IXL	BIT 5, L		LD-MB, A
110	6E	156	LD L, (HL)	LD L, (IX+d)	BIT 5, (HL)	BIT 5, (IY+d)	LD-A, MB
111	6F	157	LD L, A	LD IXL, A	BIT 5, A		RLD
112	70	160	LD (HL), B	LD (IX+d), B	BIT 6, B		

DEC	HEX	OCT	w/o prefix	DD nn	CB nn	FD CB dd nn	ED nn
113	71	161	LD (HL),C	LD (IX+d),C	BIT 6,C		
114	72	162	LD (HL),D	LD (IX+d),D	BIT 6,D		SBC HL,SP
115	73	163	LD (HL),E	LD (IX+d),E	BIT 6,E		LD (nn),SP
116	74	164	LD (HL),H	LD (IX+d),H	BIT 6,H		TSTIO n
117	75	165	LD (HL),L	LD (IX+d),L	BIT 6,L		
118	76	166	HALT		BIT 6,(HL)	BIT 6,(IY+d)	SLP
119	77	167	LD (HL),A	LD (IX+d),A	BIT 6,A		
120	78	170	LD A,B		BIT 7,B		IN A,(BC)
121	79	171	LD A,C		BIT 7,C		OUT (BC),A
122	7A	172	LD A,D		BIT 7,D		ADC HL,SP
123	7B	173	LD A,E		BIT 7,E		LD SP,(nn)
124	7C	174	LD A,H	LD A,IXH	BIT 7,H		MLT SP
125	7D	175	LD A,L	LD A,IXL	BIT 7,L		STMIX
126	7E	176	LD A,(HL)	LD A,(IX+d)	BIT 7,(HL)	BIT 7,(IY+d)	RS MIX
127	7F	177	LD A,A		BIT 7,A		
128	80	200	ADD A,B		RES 0,B		
129	81	201	ADD A,C		RES 0,C		INIM
130	82	202	ADD A,D		RES 0,D		OTIM
131	83	203	ADD A,E		RES 0,E		INI2
132	84	204	ADD A,H	ADD A,IXH	RES 0,H		
133	85	205	ADD A,L	ADD A,IXL	RES 0,L		
134	86	206	ADD A,(HL)	ADD A,(IX+d)	RES 0,(HL)	RES 0,(IY+d)	
135	87	207	ADD A,A		RES 0,A		
136	88	210	ADC A,B		RES 1,B		
137	89	211	ADC A,C		RES 1,C		INDM
138	8A	212	ADC A,D		RES 1,D		OTDM
139	8B	213	ADC A,E		RES 1,E		IND2
140	8C	214	ADC A,H	ADC A,IXH	RES 1,H		
141	8D	215	ADC A,L	ADC A,IXL	RES 1,L		
142	8E	216	ADC A,(HL)	ADC A,(IX+d)	RES 1,(HL)	RES 1,(IY+d)	
143	8F	217	ADC A,A		RES 1,A		
144	90	220	SUB A,B		RES 2,B		
145	91	221	SUB A,C		RES 2,C		INIMR
146	92	222	SUB A,D		RES 2,D		OTIMR
147	93	223	SUB A,E		RES 2,E		INI2R
148	94	224	SUB A,H	SUB A,IXH	RES 2,H		
149	95	225	SUB A,L	SUB A,IXL	RES 2,L		
150	96	226	SUB A,(HL)	SUB A,(IX+d)	RES 2,(HL)	RES 2,(IY+d)	
151	97	227	SUB A,A		RES 2,A		
152	98	230	SBC A,B		RES 3,B		
153	99	231	SBC A,C		RES 3,C		INDMR
154	9A	232	SBC A,D		RES 3,D		OTDMR
155	9B	233	SBC A,E		RES 3,E		IND2R
156	9C	234	SBC A,H	SBC A,IXH	RES 3,H		
157	9D	235	SBC A,L	SBC A,IXL	RES 3,L		
158	9E	236	SBC A,(HL)	SBC A,(IX+d)	RES 3,(HL)	RES 3,(IY+d)	
159	9F	237	SBC A,A		RES 3,A		
160	A0	240	AND B		RES 4,B		LDI
161	A1	241	AND C		RES 4,C		CPI
162	A2	242	AND D		RES 4,D		INI
163	A3	243	AND E		RES 4,E		OTI
164	A4	244	AND H	AND IXH	RES 4,H		OTI2
165	A5	245	AND L	AND IXL	RES 4,L		
166	A6	246	AND (HL)	AND (IX+d)	RES 4,(HL)	RES 4,(IY+d)	
167	A7	247	AND A		RES 4,A		
168	A8	250	XOR B		RES 5,B		LDD
169	A9	251	XOR C		RES 5,C		CPD

DEC	HEX	OCT	w/o prefix	DD nn	CB nn	FD CB dd nn	ED nn
170	AA	252	XOR D		RES 5,D		IND
171	AB	253	XOR E		RES 5,E		OTD
172	AC	254	XOR H	XOR IXH	RES 5,H		OTD2
173	AD	255	XOR L	XOR IXL	RES 5,L		
174	AE	256	XOR (HL)	XOR (IX+d)	RES 5,(HL)	RES 5,(IY+d)	
175	AF	257	XOR A		RES 5,A		
176	B0	260	OR B		RES 6,B		LDIR
177	B1	261	OR C		RES 6,C		CPIR
178	B2	262	OR D		RES 6,D		INIR
179	B3	263	OR E		RES 6,E		OTIR
180	B4	264	OR H	OR IXH	RES 6,H		OTI2R
181	B5	265	OR L	OR IXL	RES 6,L		
182	B6	266	OR (HL)	OR (IX+d)	RES 6,(HL)	RES 6,(IY+d)	
183	B7	267	OR A		RES 6,A		
184	B8	270	CP B		RES 7,B		LDDR
185	B9	271	CP C		RES 7,C		CPDR
186	BA	272	CP D		RES 7,D		INDR
187	BB	273	CP E		RES 7,E		OTDR
188	BC	274	CP H	CP IXH	RES 7,H		OTD2R
189	BD	275	CP L	CP IXL	RES 7,L		
190	BE	276	CP (HL)	CP (IX+d)	RES 7,(HL)	RES 7,(IY+d)	
191	BF	277	CP A		RES 7,A		
192	C0	300	RET NZ		SET 0,B		
193	C1	301	POP BC		SET 0,C		INIRX
194	C2	302	JP NZ,nn		SET 0,D		OTIRX
195	C3	303	JP nn		SET 0,E		
196	C4	304	CALL NZ,nn		SET 0,H		
197	C5	305	PUSH BC		SET 0,L		
198	C6	306	ADD A,n		SET 0,(HL)	SET 0,(IY+d)	
199	C7	307	RST #00		SET 0,A		LD-I,HL
200	C8	310	RET Z		SET 1,B		
201	C9	311	RET		SET 1,C		
202	CA	312	JP Z,nn		SET 1,D		INDRX
203	CB	313	*** CB ***	** DD CB **	SET 1,E		OTDRX
204	CC	314	CALL Z,nn		SET 1,H		
205	CD	315	CALL nn		SET 1,L		
206	CE	316	ADC A,n		SET 1,(HL)	SET 1,(IY+d)	
207	CF	317	RST #08		SET 1,A		
208	D0	320	RET NC		SET 2,B		
209	D1	321	POP DE		SET 2,C		
210	D2	322	JP NC,nn		SET 2,D		
211	D3	323	OUT (n),A		SET 2,E		
212	D4	324	CALL NC,nn		SET 2,H		
213	D5	325	PUSH DE		SET 2,L		
214	D6	326	SUB A,n		SET 2,(HL)	SET 2,(IY+d)	
215	D7	327	RST #10		SET 2,A		LD-HL,I
216	D8	330	RET C		SET 3,B		
217	D9	331	EXX		SET 3,C		
218	DA	332	JP C,nn		SET 3,D		
219	DB	333	IN A,(n)		SET 3,E		
220	DC	334	CALL C,nn		SET 3,H		
221	DD	335	*** DD ***		SET 3,L		
222	DE	336	SBC A,n		SET 3,(HL)	SET 3,(IY+d)	
223	DF	337	RST #18		SET 3,A		
224	E0	340	RET PO		SET 4,B		
225	E1	341	POP HL	POP IX	SET 4,C		
226	E2	342	JP PO,nn		SET 4,D		

DEC	HEX	OCT	w/o prefix	DD nn	CB nn	FD CB dd nn	ED nn
227	E3	343	EX (SP),HL	EX (SP),IX	SET 4,E		
228	E4	344	CALL P0,nn		SET 4,H		
229	E5	345	PUSH HL	PUSH IX	SET 4,L		
230	E6	346	AND n		SET 4,(HL)	SET 4,(IY+d)	
231	E7	347	RST #20		SET 4,A		
232	E8	350	RET PE		SET 5,B		
233	E9	351	JP (HL)	JP (IX)	SET 5,C		
234	EA	352	JP PE,nn		SET 5,D		
235	EB	353	EX DE,HL		SET 5,E		
236	EC	354	CALL PE,nn		SET 5,H		
237	ED	355	*** ED ***		SET 5,L		
238	EE	356	XOR n		SET 5,(HL)	SET 5,(IY+d)	
239	EF	357	RST #28		SET 5,A		
240	F0	360	RET P		SET 6,B		
241	F1	361	POP AF		SET 6,C		
242	F2	362	JP P,nn		SET 6,D		
243	F3	363	DI		SET 6,E		
244	F4	364	CALL P,nn		SET 6,H		
245	F5	365	PUSH AF		SET 6,L		
246	F6	366	OR n		SET 6,(HL)	SET 6,(IY+d)	
247	F7	367	RST #30		SET 6,A		
248	F8	370	RET M		SET 7,B		
249	F9	371	LD SP,HL	LD SP,IX	SET 7,C		
250	FA	372	JP M,nn		SET 7,D		
251	FB	373	EI		SET 7,E		
252	FC	374	CALL M,nn		SET 7,H		
253	FD	375	*** FD ***	** FD CB **	SET 7,L		
254	FE	376	CP n		SET 7,(HL)	SET 7,(IY+d)	
255	FF	377	RST #38		SET 7,A		

eZ80 instructions are bold and italic

Z180 instructions are bold

Z80 instructions are not highlighted

Notes on index registers

Where DD and IX are mentioned, FD and IY may be substituted and vice versa. If a DD or FD opcode prefixes a non-existent instruction, then the DD or FD opcode acts as a NOP and the base instruction is executed. For example, DD FF does a NOP and RST #38.

Notes on Indexed Shift/Bit Operations

A shift or bit operation on an indexed byte in memory is done by prefixing a CB opcode referring to (HL) with DD or FD to specify (IX+n) or (IY+n). If the CB opcode does not refer to (HL), then the CB prefix is executed as a NOP and the following code executed.

Notes on ED opcodes

Unsupported opcodes just increment the R register by 2.