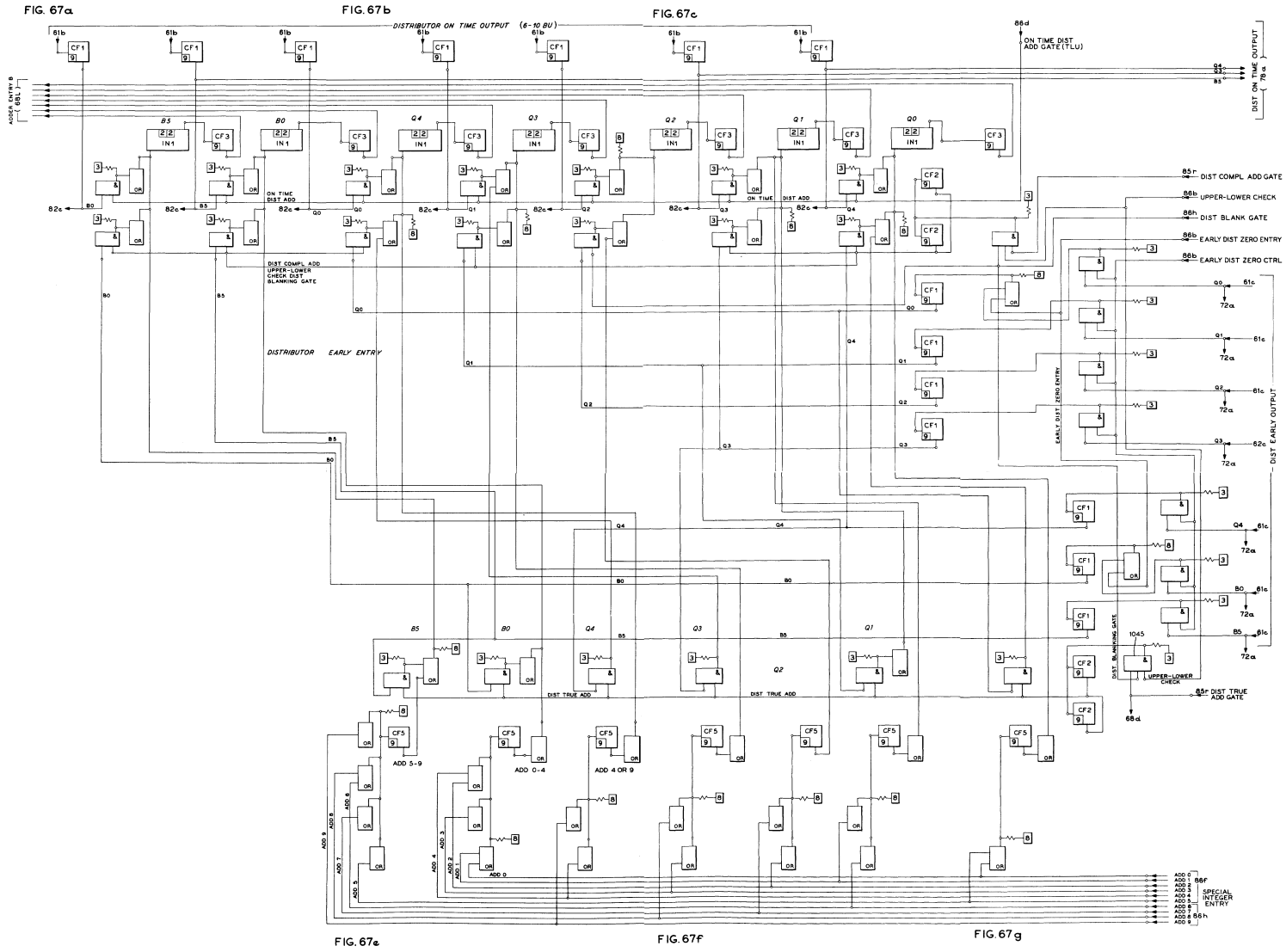
FIG. 67a
FIG. 67b
FIG. 67c
DISTRIBUTOR ON TIME OUTPUT (6-10 BU)
ON TIME DIST ADD GATE (TLU)
DIST ON TIME OUTPUT
ADDER ENTRY B
DIST COMPL ADD GATE
UPPER-LOWER CHECK
DIST BLANK GATE
EARLY DIST ZERO ENTRY
EARLY DIST ZERO CTRL
DIST COMPL. ADD
UPPER-LOWER
CHECK DIST
BLANKING GATE
DISTRIBUTOR EARLY ENTRY
EARLY DIST ZERO ENTRY
DIST EARLY OUTPUT
DIST BLANKING GATE
UPPER-LOWER CHECK
DIST TRUE ADD GATE
DIST TRUE ADD
ADD 5-9
ADD 0-4
ADD 4 OR 9
SPECIAL INTEGER ENTRY
FIG. 67e
FIG. 67f
FIG. 67g