in0
INPUT
VCC

in1
INPUT
VCC

sel1
INPUT
VCC

NOT
inst3

AND2
inst0

AND2
inst1

OR2
inst2

OUTPUT
out

| PROJECT | **A-Z80** | MODULE | **alu_mux_2** | |
|---|---|---|---|---|
| DESIGNER | Goran Devic | | | |
| DATE | June 21, 2014 | | REV | 1.0 |