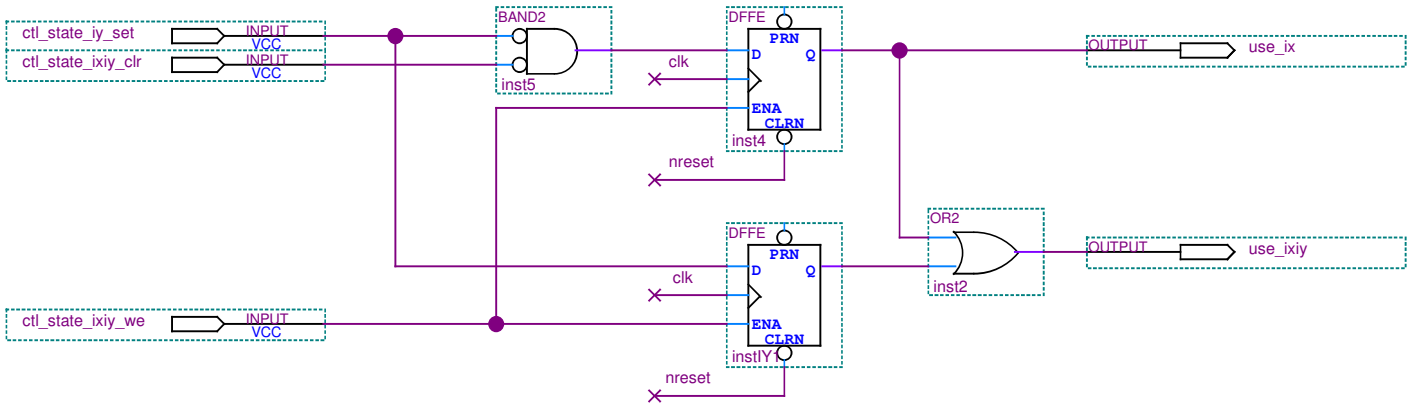
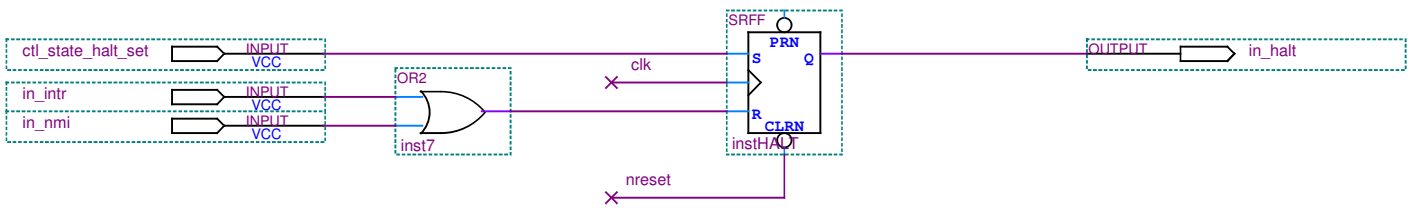


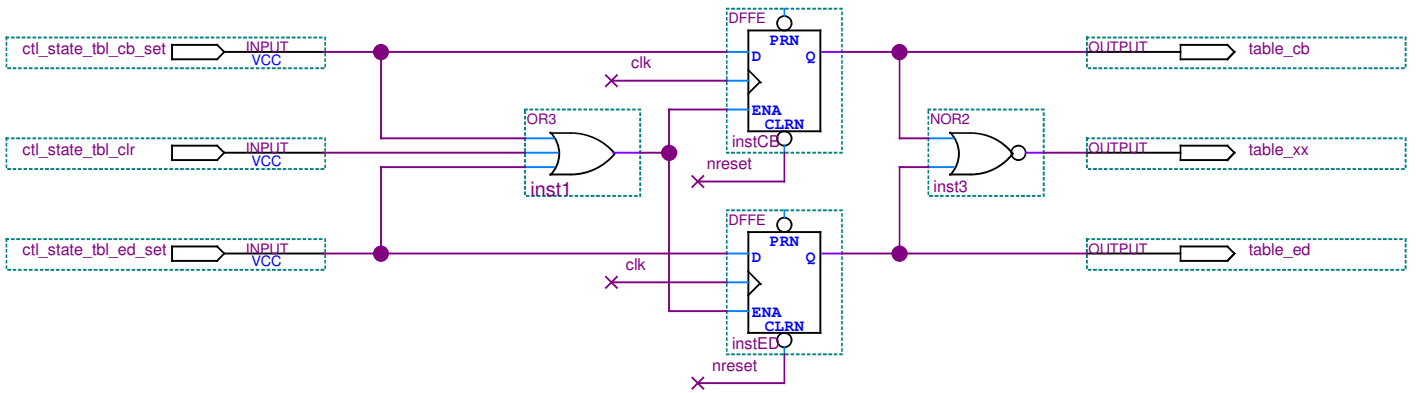
IX and IY state flags



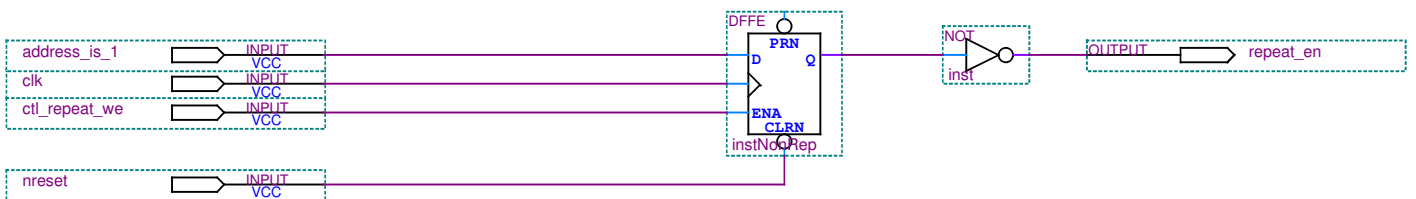
HALT state flag



Opcode Table state flags



Repeating instruction enable flag



ALU feedback wire



PROJECT	A-Z80	MODULE	decode_state
DESIGNER	Goran Devic		
DATE	August 9, 2014	REV	1.3