

## Reference List

### SDM Router

- Wei Song. Spatial Parallelism in the Routers of Asynchronous On-Chip Networks. PhD thesis, Computer Science, the University of Manchester, 2011.
- Wei Song and Doug Edwards. Asynchronous spatial division multiplexing router. *Microprocessors and Microsystems*, 2011, 35, 85-97.
- Anthony Leroy, Dragomir Milojevic, Diederik Verkest, Frédéric Robert and Francky Catthoor. Concepts and implementation of spatial division multiplexing for guaranteed throughput in networks-on-chip. *IEEE Transactions on Computers*, 2008, 57, 1182-1195.

### Channel Slicing/Lookahead

- Wei Song and Doug Edwards. A low latency wormhole router for asynchronous on-chip networks. In *Proc. of Asia and South Pacific Design Automation Conference*, 2010, 437-443.
- Wei Song and Doug Edwards. Building asynchronous routers with independent sub-channels. In *Proc. of International Symposium on System-on-Chip*, 2009, 48-51.
- Montek Singh and Steven M. Nowick. The design of high-performance dynamic asynchronous pipelines: lookahead style. *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, 2007, 15, 1256-1269.

### Asynchronous Circuits in general

- Jens Sparsø and Steve Furber. *Principles of Asynchronous Circuit Design --- A Systems Perspective*. Kluwer Academic Publishers, Boston, U.S.A, 2001

### Tree Arbiters

- Mark B. Josephs and Jelio T. Yantchev. CMOS design of the tree arbiter element. *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, 1996, 4, 472-476

### Multi-resource match allocator

- Stanislavs Golubcovs, Delong Shang, Fei Xia, Andrey Mokhov and Alex Yakovlev. Multi-resource arbiter decomposition. Tech. Report, Microelectronic System Design Group, School of EECE, Newcastle University, 2009

## **Clos Scheduler**

- Wei Song and Doug Edwards. An asynchronous routing algorithm for Clos networks In *Proc. of International Conference on Application of Concurrency to System Design*, 2010, 67-76.
- Eiji Oki, Zhigang Jing, Roberto Rojas-Cessa and H. Jonathan Chao. Concurrent round-robin-based dispatching schemes for Clos-network switches. *IEEE/ACM Transactions on Networking*, 2002, 10, 830-844.

## **Asynchronous VC router**

- Tomaz Felicijan. Quality-of-service (QoS) for asynchronous on-chip networks. PhD Thesis, Computer Science, the University of Manchester, 2004.