

EUS_100LX possible configurations

<i>Signal name</i>	<i>ETRAX 100LX</i>	<i>MCM 4+16 (full)</i>	<i>MCM 4+16 (light)</i>	<i>Schematic</i>
CFG0	-	R39 (0R)	R38 (0R)	EUS_MUX (p6) B2
CFG1	-	R41 (0R)	R40 (0R)	EUS_MUX (p6) A2
CFG2	-	R43 (0R)	R42 (0R)	EUS_MUX (p6) A2
CFG3	-	R45 (0R)	R44 (0R)	EUS_MUX (p6) A2
CFG4	-	R47 (0R)	R46(0R)	EUS_MUX (p6) A2
#PHYEN	R31 (0R)	R36 (0R)	R36 (0R)	EUS_MUX (p6) B2
#PHYRST	R34 (0R)	R35 (0R)	R35 (0R)	EUS_MUX (p6) B2
SDRAMEN	R37 (0R)	R32 (0R)	R32 (0R)	EUS_MUX (p6) B2
SDRAMCLK	R22 (33R)	R22 (33R)	R22 (33R)	EUS_MUX (p6) E3
#MRESET	R30 (0R)	-	-	EUS_MUX (p6) B1
#CSFLASH	R5 (0R)	R6 (0R)	R6 (0R)	EUS_EMIF_MEM (p3) B2
RXP	R79 (0R)	R74 (0R)	R74 (0R)	EUS_ETH (p9) D2
RXN	R80 (0R)	R73 (0R)	R73 (0R)	EUS_ETH (p9) D2
TXP	R77 (0R)	R76 (0R)	R76 (0R)	EUS_ETH (p9) D2
TXN	R78 (0R)	R75 (0R)	R75 (0R)	EUS_ETH (p9) D2
Devices				
C102	680pF	-	-	EUS_PWRETR (p10) B3
R107	8k2	-	-	EUS_PWRETR (p10) B3
C103	39pF	-	-	EUS_PWRETR (p10) B3
U25	DP83846A	-	-	EUS_ETH (p9) A2
R71	1k	-	-	EUS_ETH (p9) D1
C50	22pF	-	-	EUS_ETH (p9) D1
C51	22pF	-	-	EUS_ETH (p9) D1
X0	25MHz	-	-	EUS_ETH (p9) D1
R72	0R	-	-	EUS_ETH (p9) D1
R91	10k	-	-	EUS_ETH (p9) C2
R92	150k	-	-	EUS_ETH (p9) C2
R93	4k7	-	-	EUS_ETH (p9) B2
R94	4k7	-	-	EUS_ETH (p9) B2

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R95	4k7	-	-	EUS_ETH (p9) B3
R96	10k	-	-	EUS_ETH (p9) B2
R97	10k	-	-	EUS_ETH (p9) B2
R98	10k	-	-	EUS_ETH (p9) B3
R99	10k	-	-	EUS_ETH (p9) B3
R100	10k	-	-	EUS_ETH (p9) B3
R101	330R	-	-	EUS_ETH (p9) B3
R102	330R	-	-	EUS_ETH (p9) B3
R103	330R	-	-	EUS_ETH (p9) B3
R104	330R	-	-	EUS_ETH (p9) B3
R105	330R	-	-	EUS_ETH (p9) B3
R106	330R	-	-	EUS_ETH (p9) B3
C55	4u7/6.3V	-	-	EUS_ETH (p9) B1
C56	100nF	-	-	EUS_ETH (p9) B1
C57	470pF	-	-	EUS_ETH (p9) B1
C58	100nF	-	-	EUS_ETH (p9) B1
C59	470pF	-	-	EUS_ETH (p9) B1
C60	4u7/6.3V	-	-	EUS_ETH (p9) A1
C61	100nF	-	-	EUS_ETH (p9) A1
C62	100nF	-	-	EUS_ETH (p9) A1
C63	470pF	-	-	EUS_ETH (p9) A1
C68	4u7/6.3V	-	-	EUS_ETH (p9) A3
C67	100nF	-	-	EUS_ETH (p9) A3
C66	470pF	-	-	EUS_ETH (p9) A3
C65	100nF	-	-	EUS_ETH (p9) A2
C64	470pF	-	-	EUS_ETH (p9) A2
L2	1uH	-	-	EUS_ETH (p9) A3
C69	100nF	-	-	EUS_ETH (p9) A3

Notes:

1. Etrax 100LX – 32 MB RAM, 8 MB FLASH
2. Full version uses MCM 4+16 chip in 32-bit mode with internal and external memory, internal phyter is enabled.
3. Light version uses MCM 4+16 chip in 16-bit mode with internal memory and internal phyter.