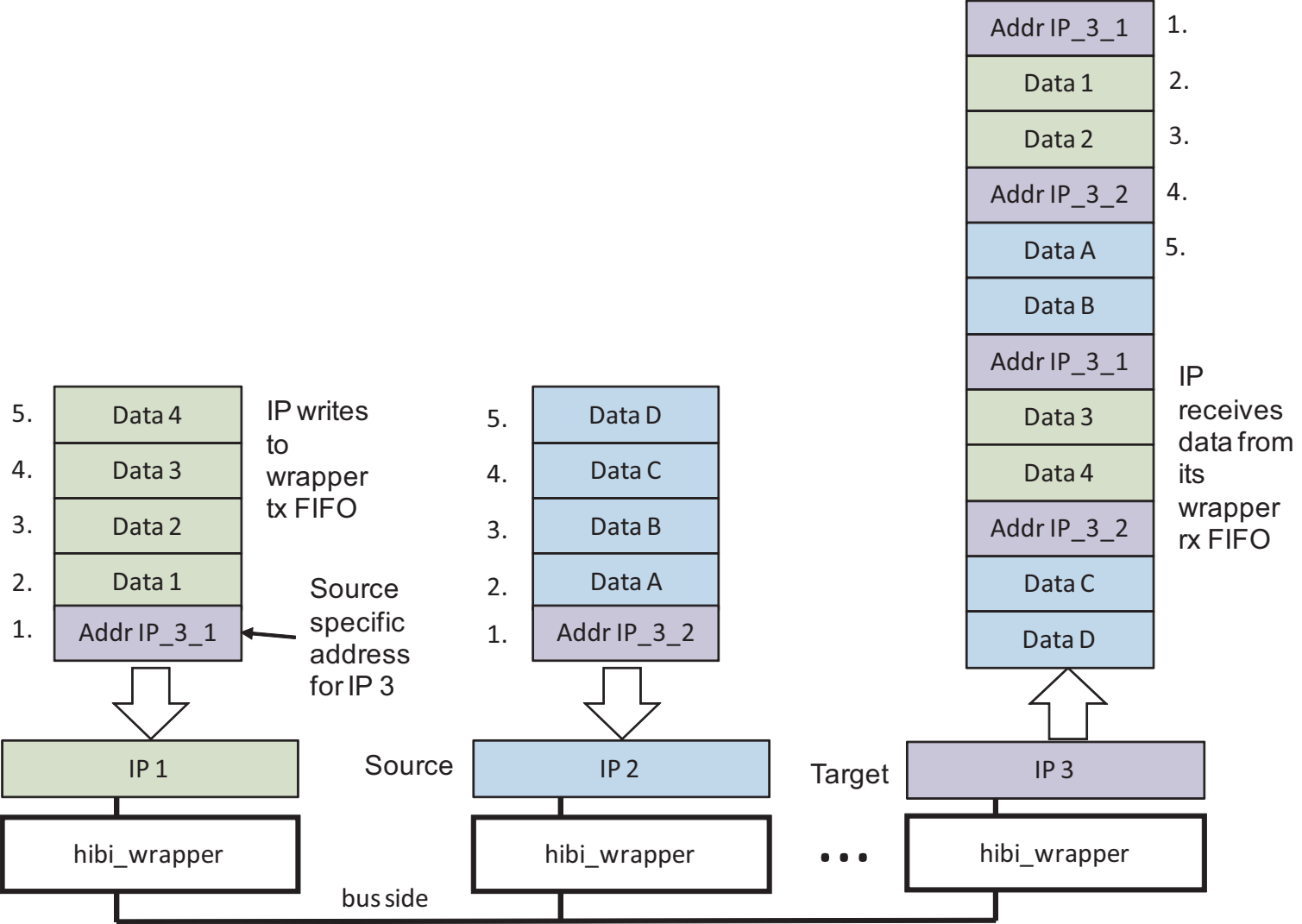
Addr IP_3_1
Data 1
Data 2
Addr IP_3_2
Data A
Data B
Addr IP_3_1
Data 3
Data 4
Addr IP_3_2
Data C
Data D
1.
2.
3.
4.
5.
IP receives data from its wrapper rx FIFO
5. Data 4
4. Data 3
3. Data 2
2. Data 1
1. Addr IP_3_1
IP writes to wrapper tx FIFO
Source specific address for IP 3
5. Data D
4. Data C
3. Data B
2. Data A
1. Addr IP_3_2
IP 1
Source
IP 2
Target
IP 3
hibi_wrapper
hibi_wrapper
hibi_wrapper
bus side