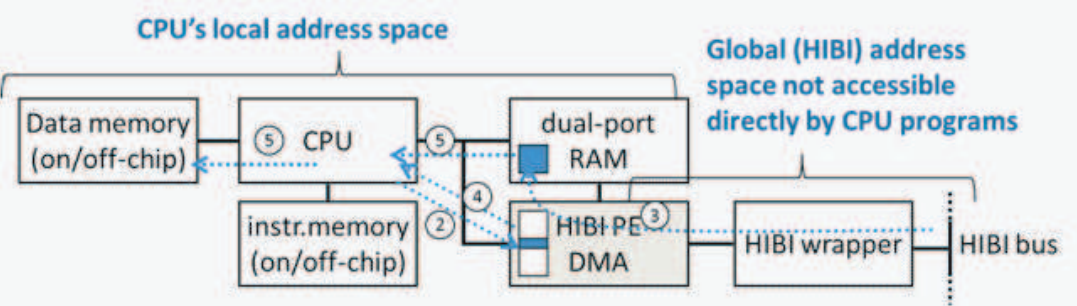
CPU's local address space
Global (HIBI) address space not accessible directly by CPU programs
Data memory (on/off-chip)
5
CPU
5
dual-port RAM
4
2
instr.memory (on/off-chip)
HIBI PE
3
DMA
HIBI wrapper
HIBI bus