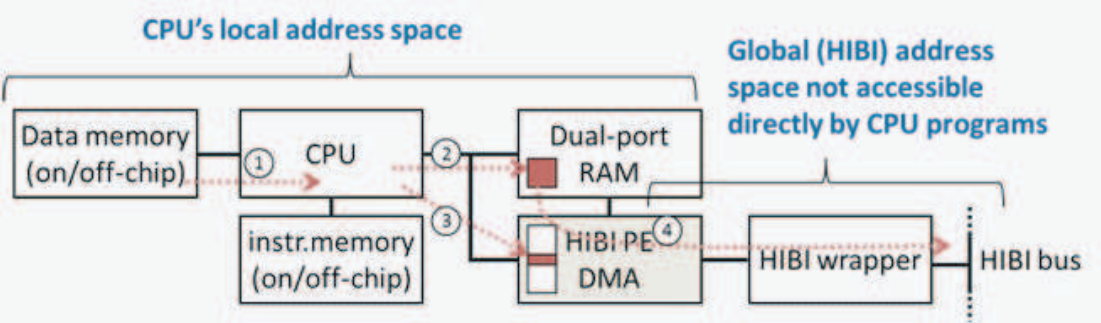
CPU's local address space
Global (HIBI) address space not accessible directly by CPU programs
Data memory (on/off-chip)
1
CPU
2
Dual-port RAM
3
instr.memory (on/off-chip)
HiBI PE
DMA
4
HIBI wrapper
HIBI bus