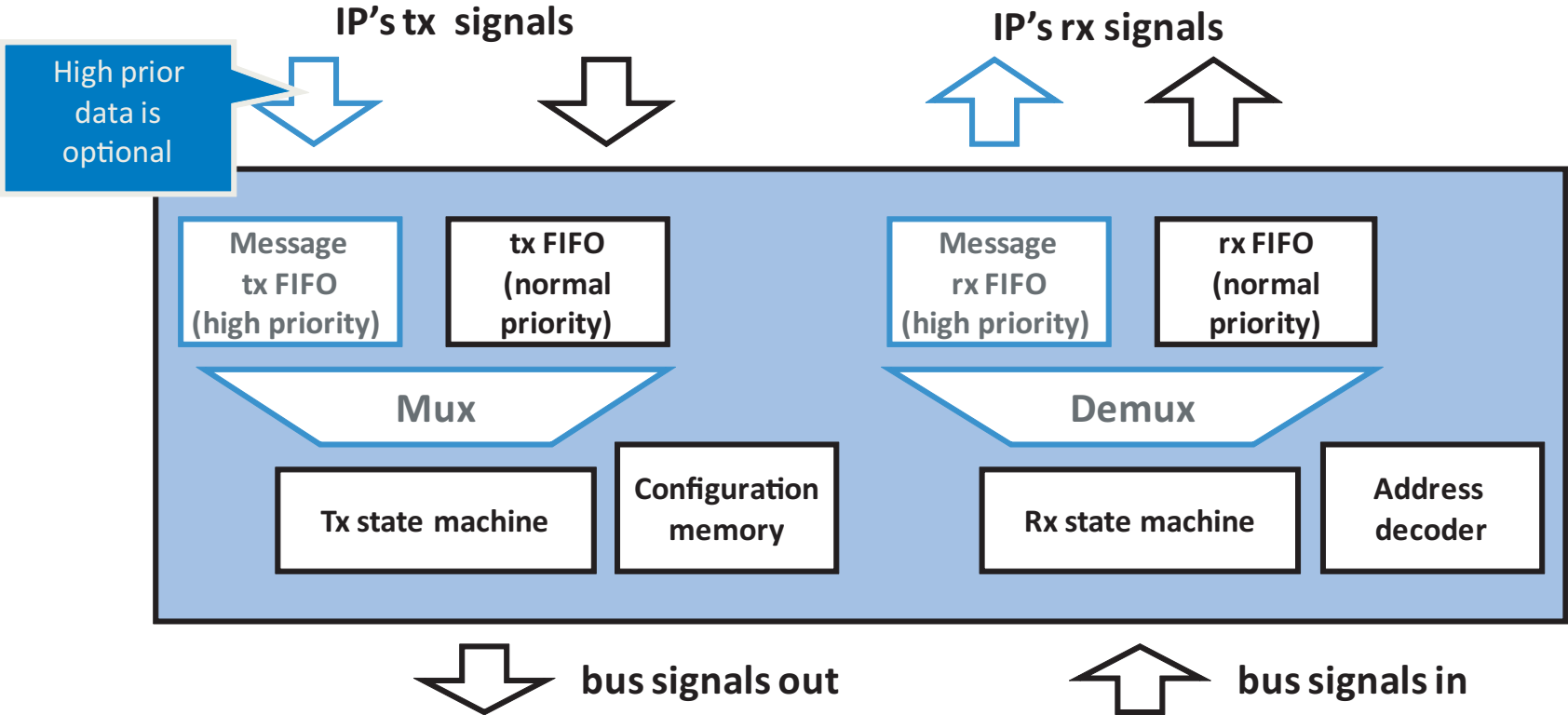
IP's tx signals
IP's rx signals
High prior data is optional
Message tx FIFO (high priority)
tx FIFO (normal priority)
Message rx FIFO (high priority)
rx FIFO (normal priority)
Mux
Demux
Tx state machine
Configuration memory
Rx state machine
Address decoder
bus signals out
bus signals in