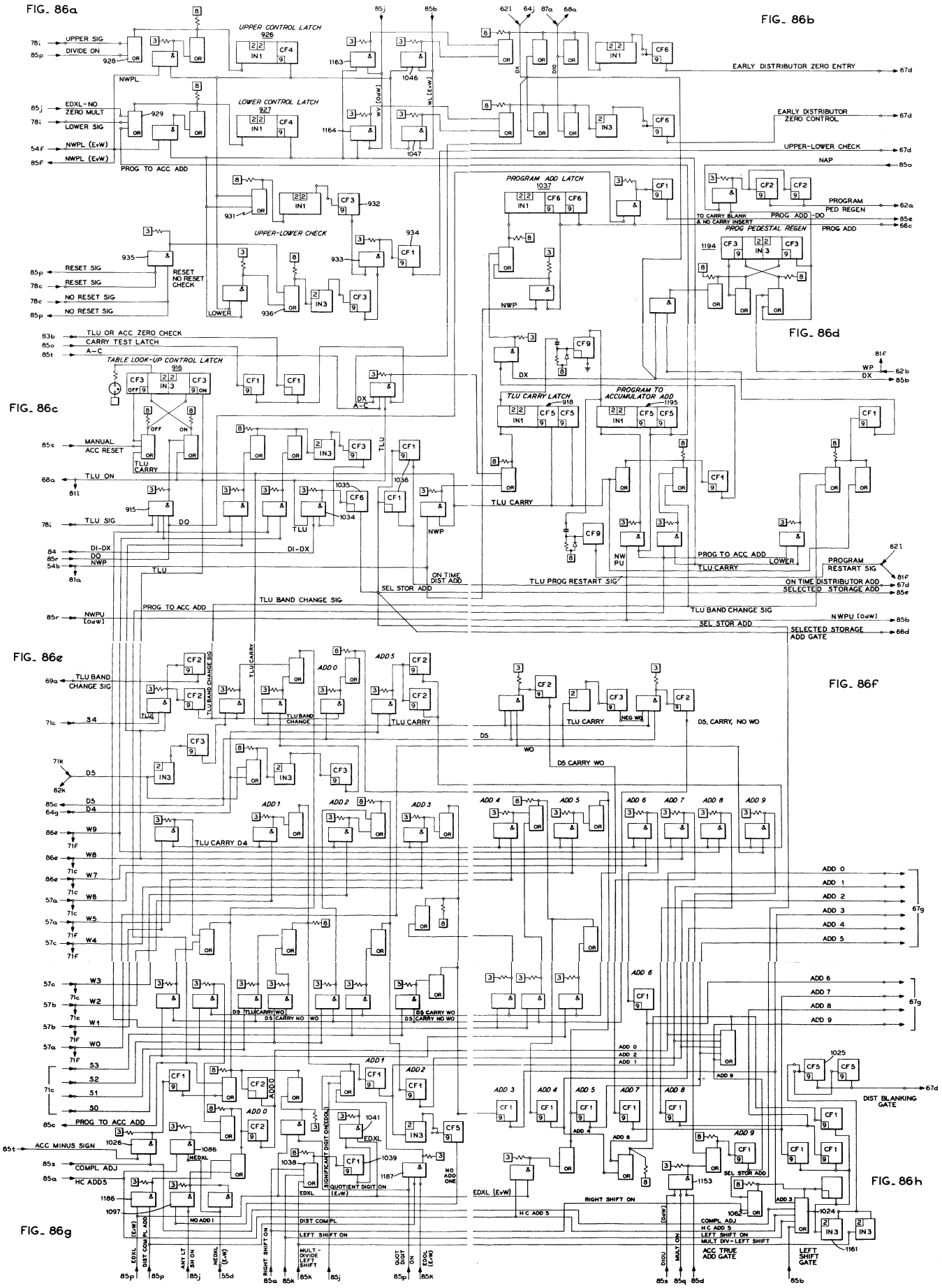
FIG. 86a
FIG. 86b
FIG. 86c
FIG. 86d
FIG. 86e
FIG. 86f
FIG. 86g
FIG. 86h
UPPER CONTROL LATCH 926
LOWER CONTROL LATCH 927
UPPER SIG
DIVIDE ON
EDXL-NO ZERO MULT
LOWER SIG
NWPL (EvW)
PROG TO ACC ADD
EARLY DISTRIBUTOR ZERO ENTRY
EARLY DISTRIBUTOR ZERO CONTROL
UPPER-LOWER CHECK
NAP
PROGRAM ADD LATCH 1037
PROGRAM PED REGEN
PROG ADD-DO
PROG PEDESTAL REGEN
PROG ADD
RESET SIG
NO RESET SIG
RESET NO RESET CHECK
TLU OR ACC ZERO CHECK
CARRY TEST LATCH
A-C
TABLE LOOK-UP CONTROL LATCH 916
MANUAL ACC RESET
TLU CARRY
TLU ON
TLU SIG
DI-DX
DO
NWP
TLU CARRY LATCH 918
PROGRAM TO ACCUMULATOR ADD 1195
PROG TO ACC ADD
PROGRAM RESTART SIG
ON TIME DISTRIBUTOR ADD
SELECTED STORAGE ADD
TLU BAND CHANGE SIG
NWPU [OdW]
SEL STOR ADD
SELECTED STORAGE ADD GATE
TLU BAND CHANGE SIG
TLU CARRY D4
D5, CARRY, NO WO
D5 CARRY WO
ADD 0
ADD 1
ADD 2
ADD 3
ADD 4
ADD 5
ADD 6
ADD 7
ADD 8
ADD 9
DIST BLANKING GATE
ACC MINUS SIGN
COMPL ADJ
HC ADD5
NO ADD 1
DIST COMPL
LEFT SHIFT ON
RIGHT SHIFT ON
MULT-DIVIDE LEFT SHIFT
QUOTIENT DIGIT ON (EvW)
NO ADD ONE
HC ADD 5
MULT DIV-LEFT SHIFT
ACC TRUE ADD GATE
LEFT SHIFT GATE