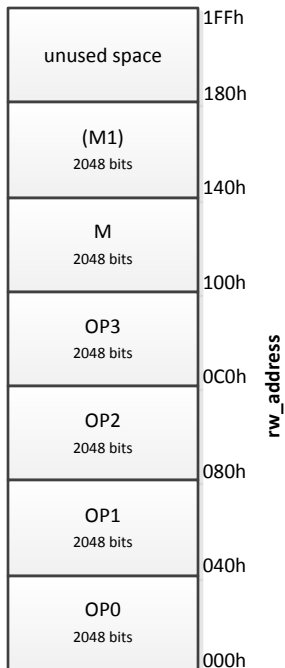


mod_sim_exp_core
memory mapping
(32-bit word addressing)



address structure:

bit: 8 -> '1': modulus

'0': operands

bits: 7-6 -> operand select in case of bit 8 = '0'

modulus select in case of bit 8 = '1'

bits: 5-0 -> modulus addr / operand addr resp.

Note: modulus M1 is optional, and only available when C_MEM_STYLE =
generic or asym