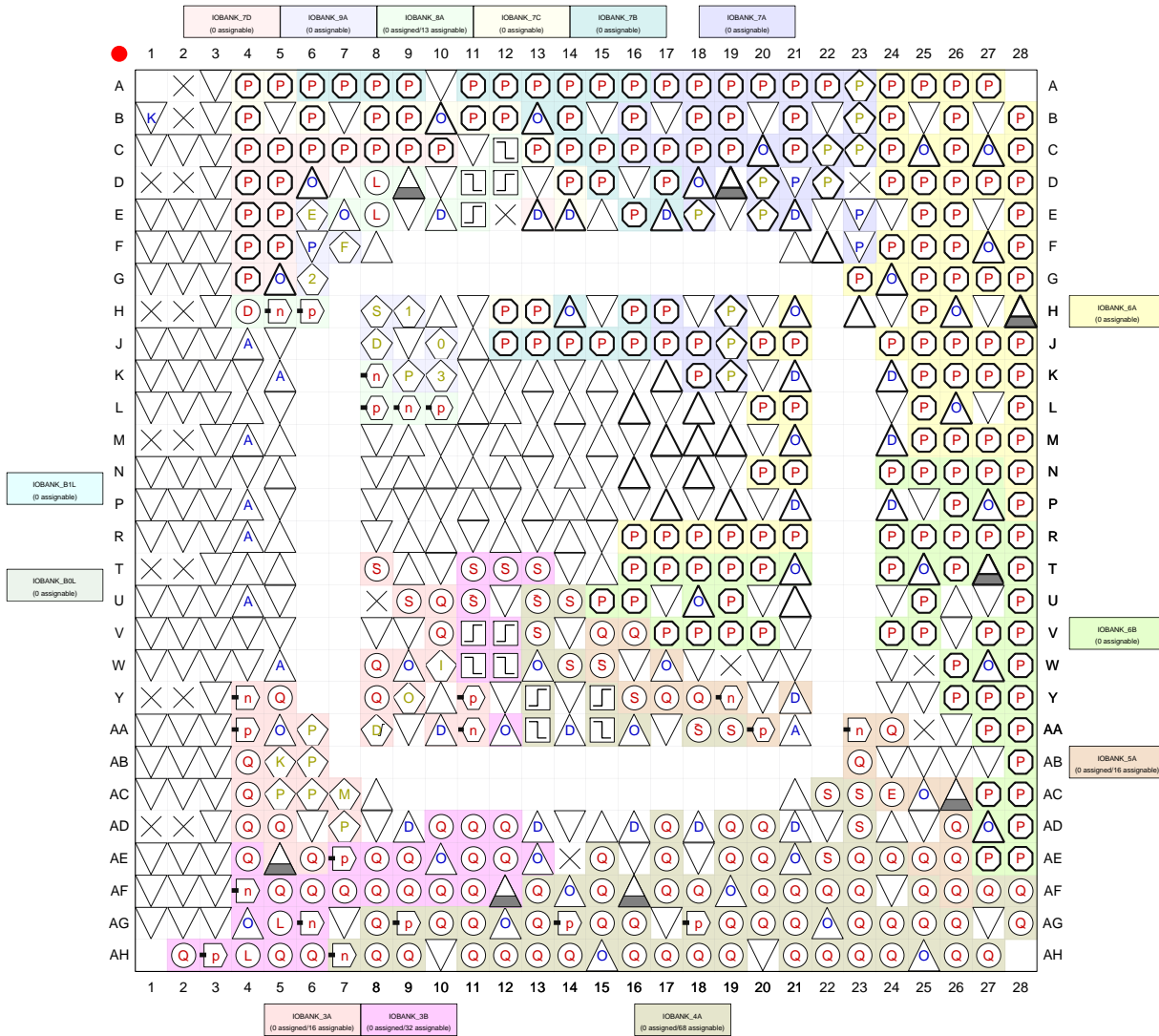


# Top View - Wire Bond

## Cyclone V - 5CSEMA4U23C6 Preliminary



Legend:

- |  |                     |  |                           |
|--|---------------------|--|---------------------------|
|  | User I/O            |  | User assigned I/O         |
|  | Filter assigned I/O |  | Unbonded pad              |
|  | Reserved pin        |  | DEV_OE                    |
|  | DIFF_n              |  | DIFF_p                    |
|  | DIFF_n output       |  | DIFF_p output             |
|  | DQ                  |  | DQS                       |
|  | DQSB                |  | Hard processor system pir |
|  | CLK_n               |  | Other dual purpose        |
|  | Other PLL           |  | MSEL1                     |
|  | MSEL0               |  | MSEL2                     |
|  | MSEL2               |  | MSEL3                     |
|  | CONF_DONE           |  | DCLK                      |
|  | nCE                 |  | nCONFIG                   |
|  | TDI                 |  | TCK                       |
|  | TMS                 |  | TDO                       |
|  | nSTATUS             |  | Other dedicated programm  |
|  | VREF                |  | VCCP/VCCR/VCCT            |
|  | VCCA                |  | VCCIO                     |
|  | VCCPD               |  | GND                       |
|  | GND for programming |  | RREF                      |
|  | No connect          |  |                           |