Process Properties - Configuration Options

Category

- General Options
- Configuration Options
- Startup Options
- Readback Options
- Encryption Options
- Suspend/Wake Options

Switch Name	Property Name	Value
-g ConfigRate	Configuration Rate	16
-g ProgPin	Configuration Pin Program	Pull Up
-g DonePin	Configuration Pin Done	Pull Up
-g TckPin	JTAG Pin TCK	Pull Up
-g TdiPin	JTAG Pin TDI	Pull Up
-g TdoPin	JTAG Pin TDO	Pull Up
-g TmsPin	JTAG Pin TMS	Pull Up
-g UnusedPin	Unused IOB Pins	Pull Down
-g UserID	UserID Code (8 Digit Hexadecimal)	0xFFFFFFFF
-g ExtMasterCclk_en	Enable External Master Clock	☐
-g ExtMasterCclk_divide	Setup External Master Clock Division	1
-g SPI_buswidth	Set SPI Configuration Bus Width	1
-g TIMER_CFG	Watchdog Timer Value	0xFFFF
	Place MultiBoot Settings into Bitstream	☐
-g next_config_reboot	MultiBoot Insert IPROG CMD in the Bitfile	Enable
-g next_config_addr	MultiBoot Starting Address for Next Configuration	0x00000000
-g next_config_new_mode	MultiBoot Use New Mode for Next Configuration	☑
-g next_config_boot_mode	MultiBoot Next Configuration Mode	001
-g golden_config_addr	MultiBoot Starting Address for Golden Configuration	0x00000000
-g failsafe_user	MultiBoot User-Defined Register for Failsafe Scheme	0x0000

Property display level: Advanced ☑ Display switch names Default

OK Close Apply Help