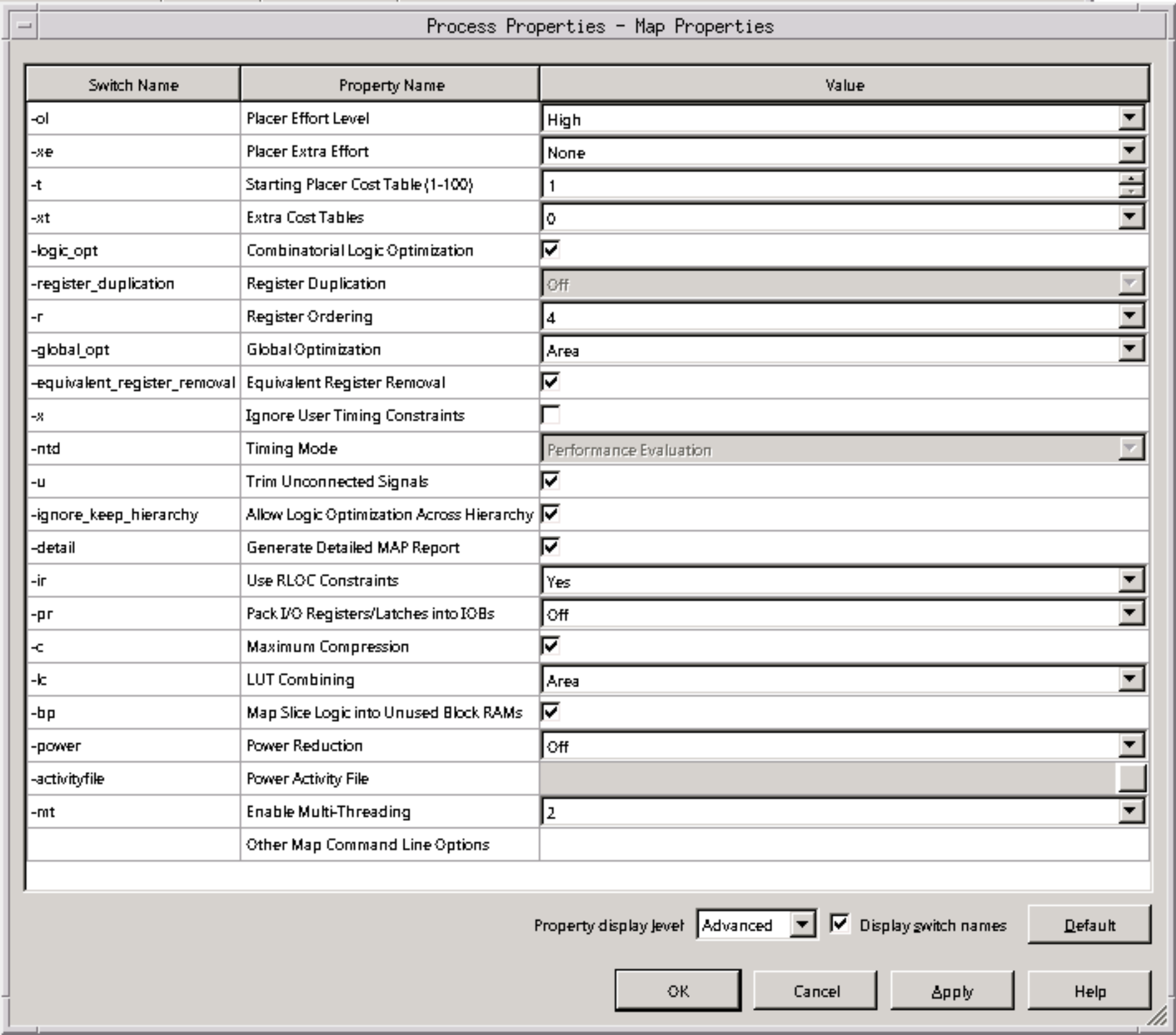
Process Properties - Map Properties
Switch Name
Property Name
Value
-ol
Placer Effort Level
High
-xe
Placer Extra Effort
None
-t
Starting Placer Cost Table (1-100)
1
-xt
Extra Cost Tables
0
-logic_opt
Combinatorial Logic Optimization
-register_duplication
Register Duplication
Off
-r
Register Ordering
4
-global_opt
Global Optimization
Area
-equivalent_register_removal
Equivalent Register Removal
-x
Ignore User Timing Constraints
-ntd
Timing Mode
Performance Evaluation
-u
Trim Unconnected Signals
-ignore_keep_hierarchy
Allow Logic Optimization Across Hierarchy
-detail
Generate Detailed MAP Report
-ir
Use RLOC Constraints
Yes
-pr
Pack I/O Registers/Latches into IOBs
Off
-c
Maximum Compression
-lc
LUT Combining
Area
-bp
Map Slice Logic into Unused Block RAMs
-power
Power Reduction
Off
-activityfile
Power Activity File
-mt
Enable Multi-Threading
2
Other Map Command Line Options
Property display level
Advanced
Display switch names
Default
OK
Cancel
Apply
Help