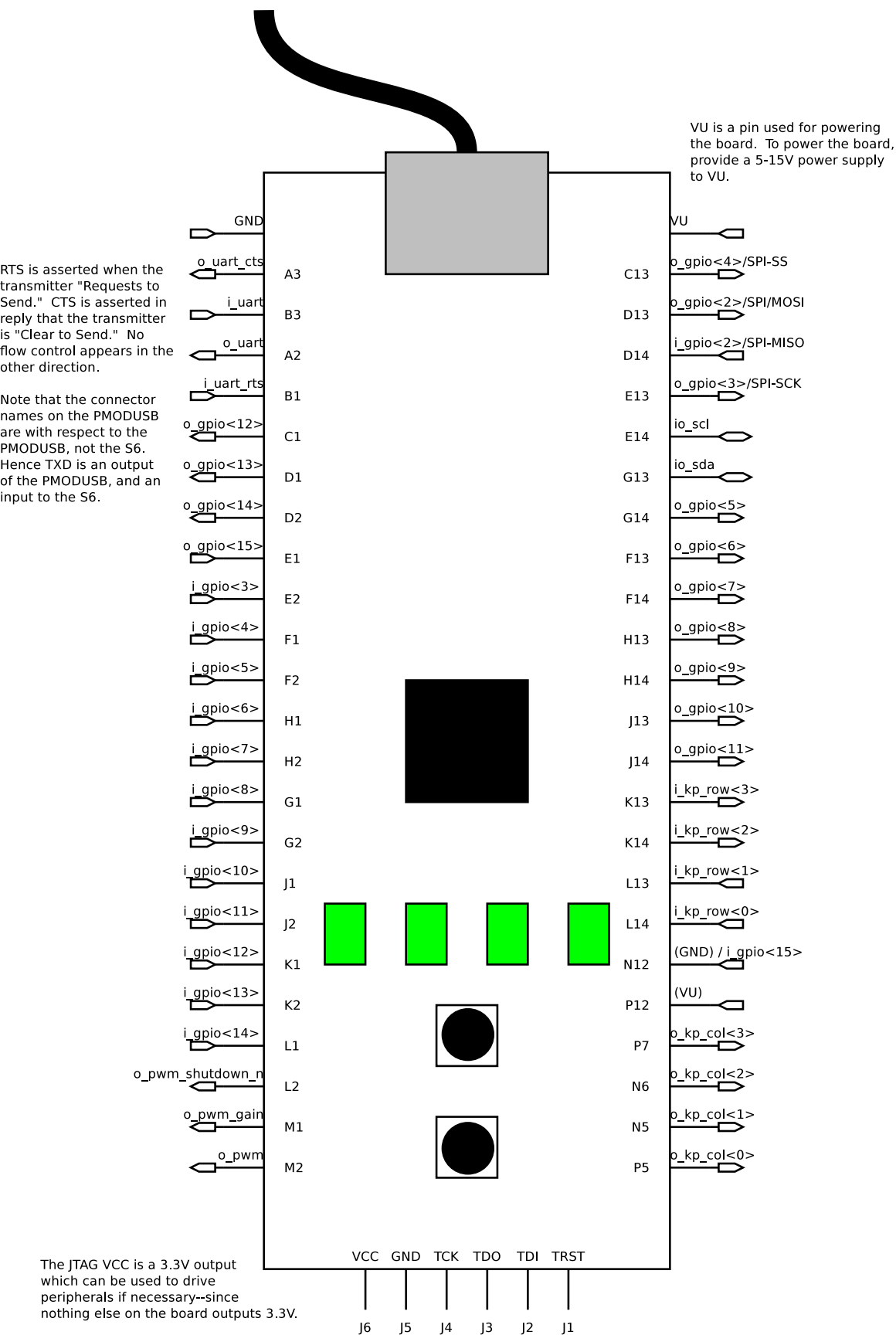
VU is a pin used for powering the board. To power the board, provide a 5-15V power supply to VU.
RTS is asserted when the transmitter "Requests to Send." CTS is asserted in reply that the transmitter is "Clear to Send." No flow control appears in the other direction.
Note that the connector names on the PMODUSB are with respect to the PMODUSB, not the S6. Hence TXD is an output of the PMODUSB, and an input to the S6.
GND
o_uart_cts
i_uart
o_uart
i_uart_rts
o_gpio<12>
o_gpio<13>
o_gpio<14>
o_gpio<15>
i_gpio<3>
i_gpio<4>
i_gpio<5>
i_gpio<6>
i_gpio<7>
i_gpio<8>
i_gpio<9>
i_gpio<10>
i_gpio<11>
i_gpio<12>
i_gpio<13>
i_gpio<14>
o_pwm_shutdown_n
o_pwm_gain
o_pwm
A3
B3
A2
B1
C1
D1
D2
E1
E2
F1
F2
H1
H2
G1
G2
J1
J2
K1
K2
L1
L2
M1
M2
VU
o_gpio<4>/SPI-SS
o_gpio<2>/SPI/MOSI
i_gpio<2>/SPI-MISO
o_gpio<3>/SPI-SCK
io_scl
io_sda
o_gpio<5>
o_gpio<6>
o_gpio<7>
o_gpio<8>
o_gpio<9>
o_gpio<10>
o_gpio<11>
i_kp_row<3>
i_kp_row<2>
i_kp_row<1>
i_kp_row<0>
(GND) / i_gpio<15>
(VU)
o_kp_col<3>
o_kp_col<2>
o_kp_col<1>
o_kp_col<0>
C13
D13
D14
E13
E14
G13
G14
F13
F14
H13
H14
J13
J14
K13
K14
L13
L14
N12
P12
P7
N6
N5
P5
VCC
GND
TCK
TDO
TDI
TRST
J6
J5
J4
J3
J2
J1
The JTAG VCC is a 3.3V output which can be used to drive peripherals if necessary--since nothing else on the board outputs 3.3V.