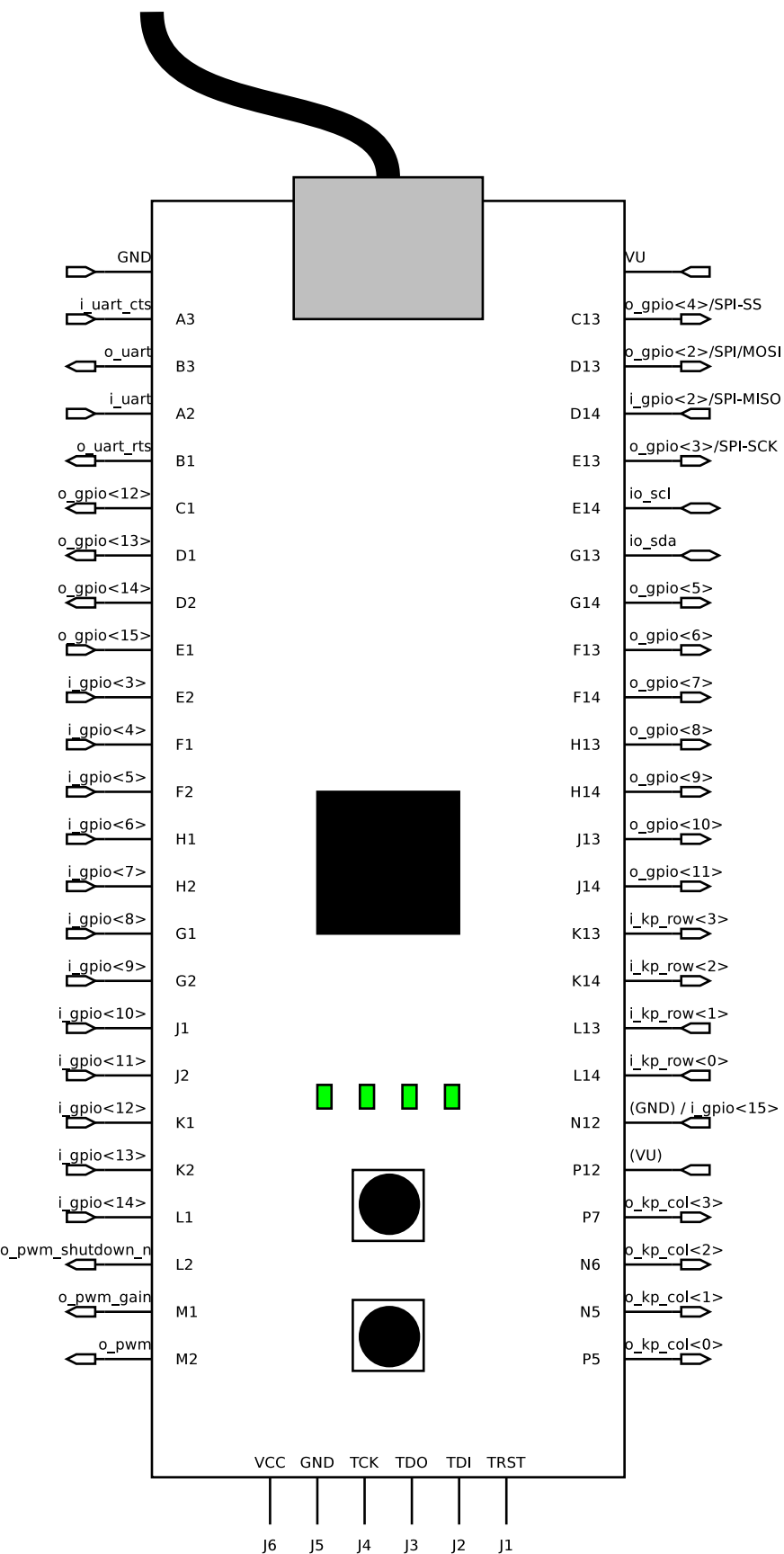
GND
i_uart_cts
A3
o_uart
B3
i_uart
A2
o_uart_rts
B1
o_gpio<12>
C1
o_gpio<13>
D1
o_gpio<14>
D2
o_gpio<15>
E1
i_gpio<3>
E2
i_gpio<4>
F1
i_gpio<5>
F2
i_gpio<6>
H1
i_gpio<7>
H2
i_gpio<8>
G1
i_gpio<9>
G2
i_gpio<10>
J1
i_gpio<11>
J2
i_gpio<12>
K1
i_gpio<13>
K2
i_gpio<14>
L1
o_pwm_shutdown_n
L2
o_pwm_gain
M1
o_pwm
M2
VU
C13
o_gpio<4>/SPI-SS
D13
o_gpio<2>/SPI/MOSI
D14
i_gpio<2>/SPI-MISO
E13
o_gpio<3>/SPI-SCK
E14
io_scl
G13
io_sda
G14
o_gpio<5>
F13
o_gpio<6>
F14
o_gpio<7>
H13
o_gpio<8>
H14
o_gpio<9>
J13
o_gpio<10>
J14
o_gpio<11>
K13
i_kp_row<3>
K14
i_kp_row<2>
L13
i_kp_row<1>
L14
i_kp_row<0>
N12
(GND) / i_gpio<15>
P12
(VU)
P7
o_kp_col<3>
N6
o_kp_col<2>
N5
o_kp_col<1>
P5
o_kp_col<0>
VCC
GND
TCK
TDO
TDI
TRST
J6
J5
J4
J3
J2
J1