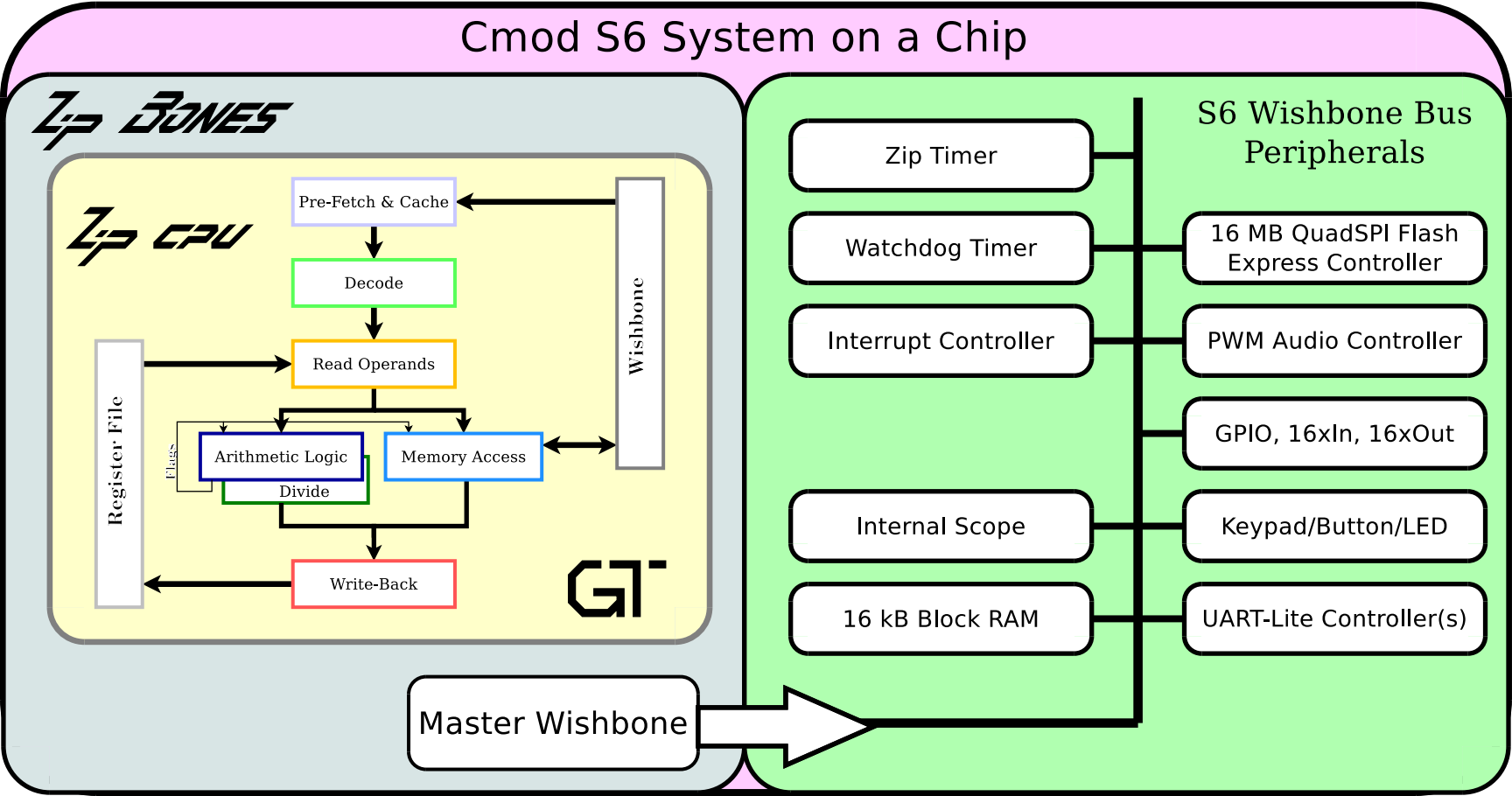
Cmod S6 System on a Chip
Zip Bones
Zip CPU
Pre-Fetch & Cache
Decode
Read Operands
Arithmetic Logic
Memory Access
Divide
Flags
Register File
Wishbone
Write-Back
GT
Master Wishbone
S6 Wishbone Bus Peripherals
Zip Timer
Watchdog Timer
Interrupt Controller
Internal Scope
16 kB Block RAM
16 MB QuadSPI Flash Express Controller
PWM Audio Controller
GPIO, 16xIn, 16xOut
Keypad/Button/LED
UART-Lite Controller(s)