Counter Library

Novakod Studio is the integrated development environment (IDE) for the psC language.

- Cores in this library use the psC language.
- Compiling the core with the IDE generates Quality VHDL code.
- The generated VHDL code can be used in any FPGA project.
- You can use the cores as is, or create your own customized core.
- You can also use Novakod Studio for any FPGA applications, then generate VHDL.

Materials

You need Novakod Studio and the DE1SoC BSP (Board Support Package)

- Download at: https://icitechno.com/download
- Licenses at: <u>https://icitechno.com/licenses</u>

If you want to experiment with a real board, you need the DE1SoC board. This board is fully integrated into Novakod Studio. *psC programs run without modification on the real board*.

- Buy at: <u>Terasic</u>

VERY IMPORTANT

Folder paths and file names you create must not contain spaces or special characters.

<u>To begin with...</u>

First double-click CopyLib.bat

- 1. Open the library: <u>C:\Novakod_Studio\OpenCoresLib\simple_customized_counter\CounterLib.psC</u>
- 2. Read the first part.
- 3. Have a look at the code of the four counter templates.
- 4. Select the test bench for the desired core.

Core	Test bench
CCounterEvent_T	TestCounterEvent
CCounterLevel_T	TestCounterLevel
CCounterOprLevel_T	TestCounter0prLevel
CCounterOprEvent_T	TestCounterOprEventBoard TestCounterOprEventAPI

5. Follow ReadMe.pdf in the selected folder.

Have fun!

Basic coding rules

Print this page ...

The psC language is based on C++ syntax and everything you learned about designing, coding and documenting C++ programs can be used with psC. You should look at the examples to get a good feeling for the coding style.

- Naming convention

As in C++, carefully choose names for variables, ports, functions, components, and so on, to reflect their usage. This must be done as early as possible as it greatly improves readability. In psC, the recommended naming convention is capitalized first word letter, like ExeOpr.

- Indentation

Indentation of 4 spaces, no tabulation, is recommended for compatibility between editors.

- Suffixes and prefixes

Here is a list of prefixes and suffixes specific to psC. You should use them systematically.

Prefix	Suffix	Apply to	Example
С		Component	// Component Ports
Р		Process	component <mark>C</mark> Test (in int <mark>i</mark> P, out int <mark>o</mark> P)
i		Input port	{ };
0		Output port	<mark>C</mark> Test <mark>P</mark> Test; // Process
	_t	Туре	typedef int:3 int3 <mark>_t</mark> ;
с		Constant	<pre>const int cLines[] = 1 to 2; const int cCols [] = 1 to 3; const identifier cId[] = { A, B, C }; enum Color_t { cRed, cGreen, cBlue };</pre>
t		Temp variable	temp $\frac{t}{t}$ Add = V1 + V2;
р		Parameters	<pre>temp fix8 tAdd(fix8 pF0, fix8 pF1) = pF0 + pF1; function fct(int pVal, ubyte pTyp) { };</pre>
	_T	Template	
	_I	Template instance	<pre>template< int NVAL, identifier NAME > function Add_T() { };</pre>
All Caps	5	Template parameters	<pre>function Add_T<8, oPort> Add_I;</pre>
One to t capital		Forend parameters	<pre>for I in <crange> CInc PInc##I; end</crange></pre>
s_ g_		Reserved, do no	t use