DDR3 simple test

Edit the project DDR3_1.xise

Change this line :

```
when "00010"=>
    c3_p0_wr_data <=  "00000000000000000000000000000101";
    state <= "00011";
```

The last 3 bits of this value are shown on the on-board leds after a DDR3 read / write test.

power led

led<0>

led<1>

led<2>