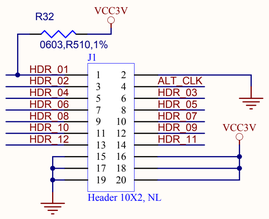
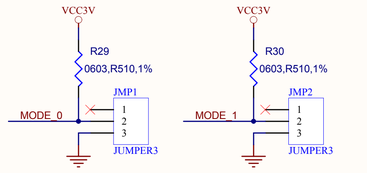
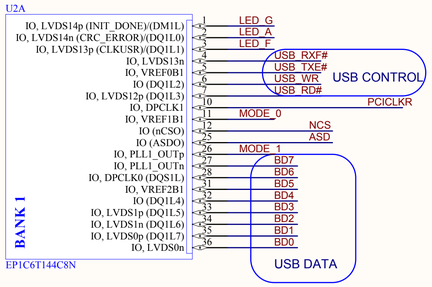
UNUSED EP1C6T144C8N I/O PINS

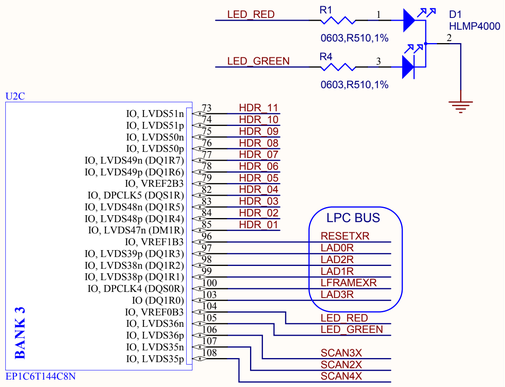


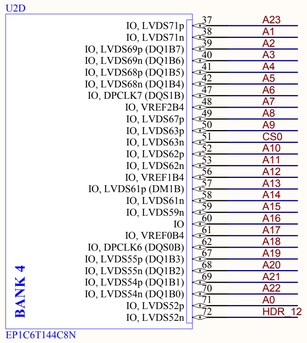
MODE SELECT

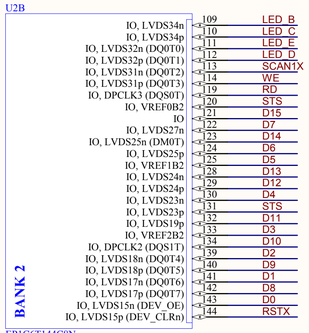


EP1C6T144C8N I/O

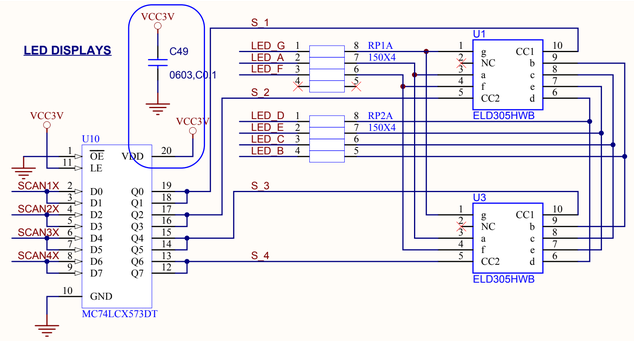




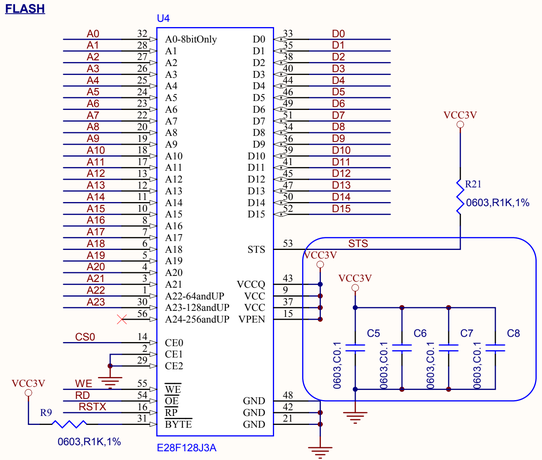




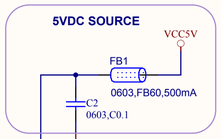
LED DISPLAYS



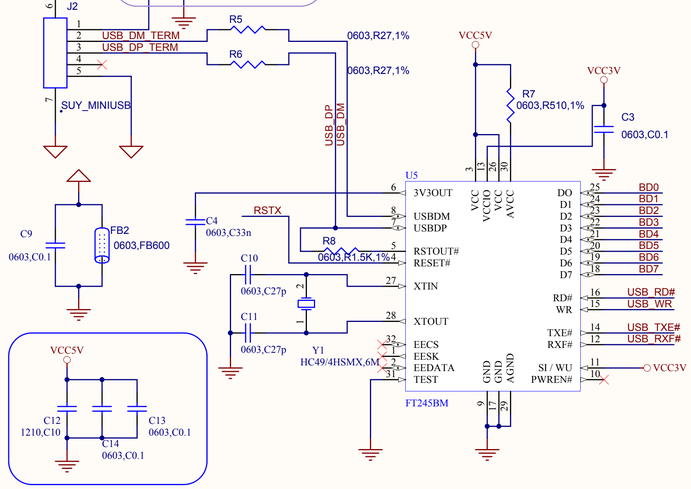
FLASH



5VDC SOURCE

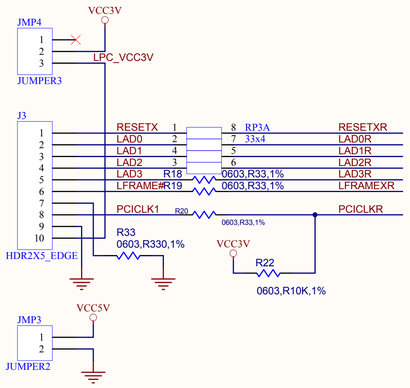


USB INTERFACE

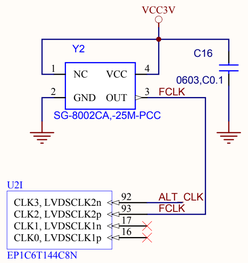


FT245BM (U5) Supply Voltage Decoupling Capacitors

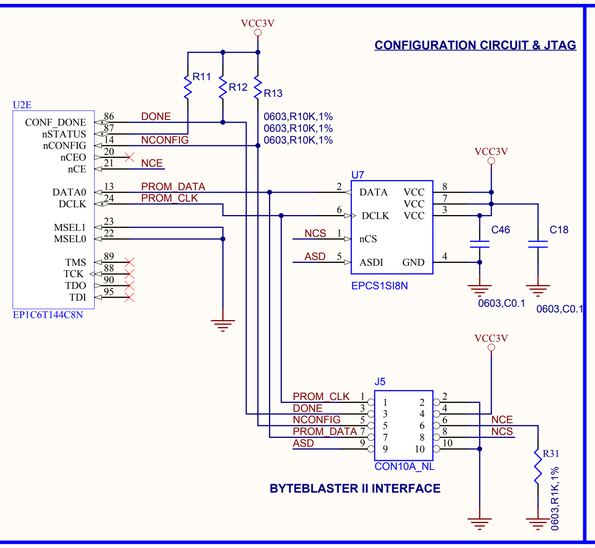
LPC PORT



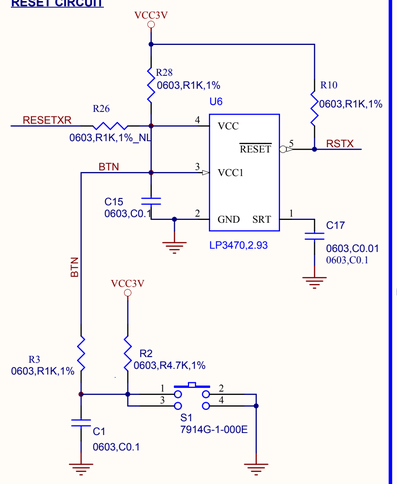
FPGA CLOCK



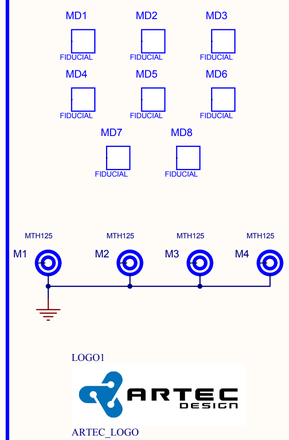
CONFIGURATION CIRCUIT & JTAG



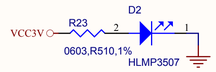
RESET CIRCUIT

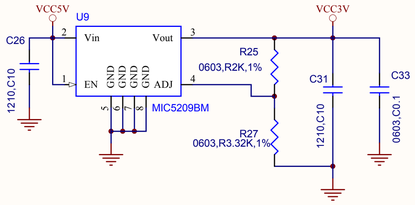


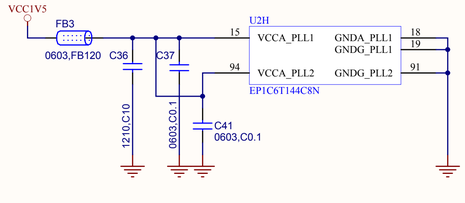
MECHANICAL & MISC.

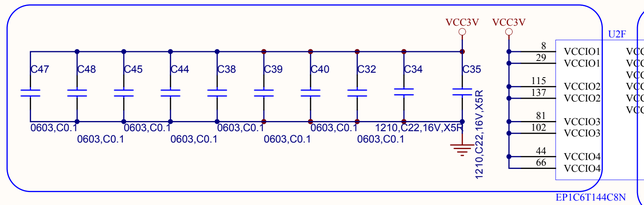


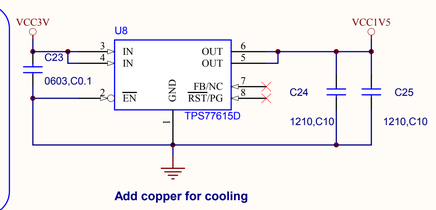
POWER SECTIONS

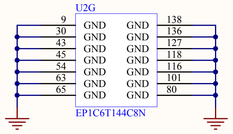












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APPROVALS	Date
Drawn: Jan Zavitski	02/01/07
Checked: Kaido Kevvai	02/01/07
Approved: Kaido Kevvai	02/01/07

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Rel: 01
Size: C
Document nr: 90441104-01
Document name: 441104_DONGLE_ALTERA
Rev: 04
Date:
Sheet 1 of 1

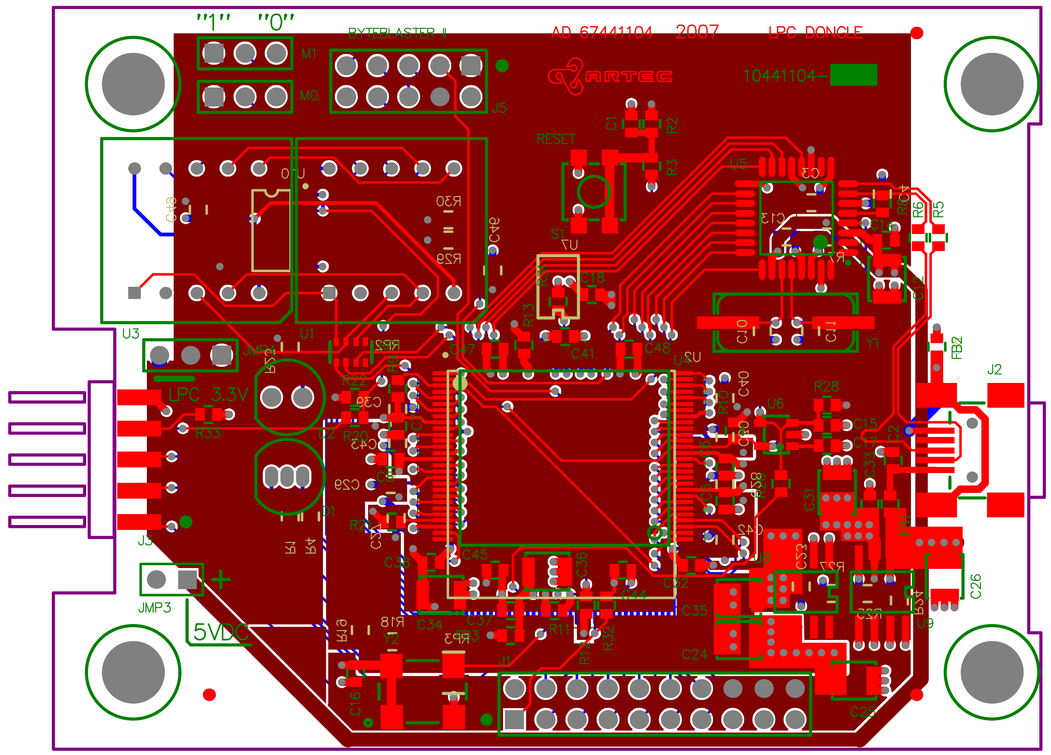
ROUTING BOTTOM L(4)

POWER PLANE L(3)

ROUTING TOP(L1) 60.5X81MM

SILKSCREEN TOP (L26)

SILKSCREEN BOTTOM (L29)



ARTEC DESIGN

LPC DONGLE

67441104

8.06.2007

TOP LAYER

POWER PLANE

BOTTOM LAYER