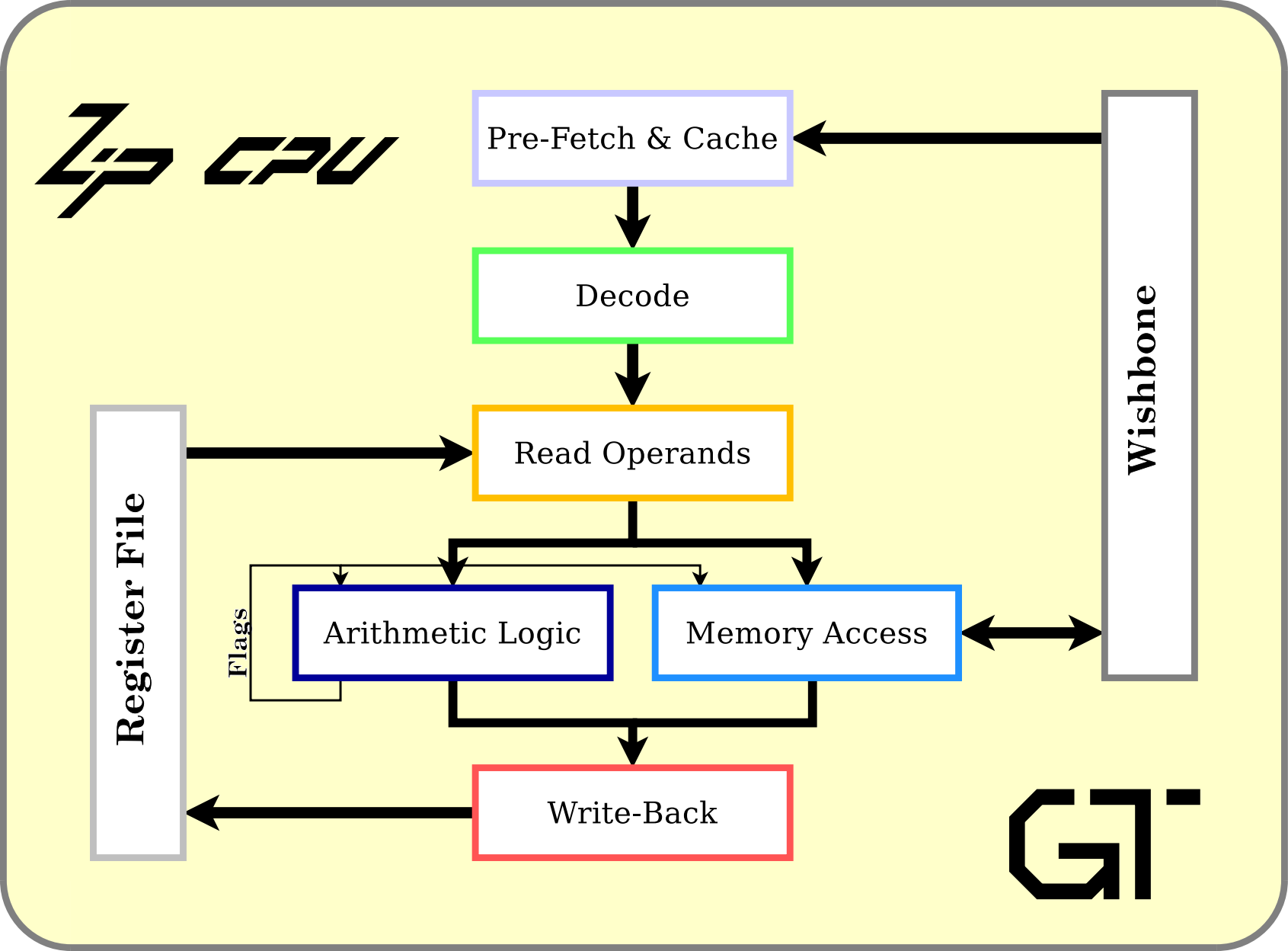
Zip CPU
Pre-Fetch & Cache
Decode
Read Operands
Arithmetic Logic
Memory Access
Write-Back
Flags
Register File
Wishbone