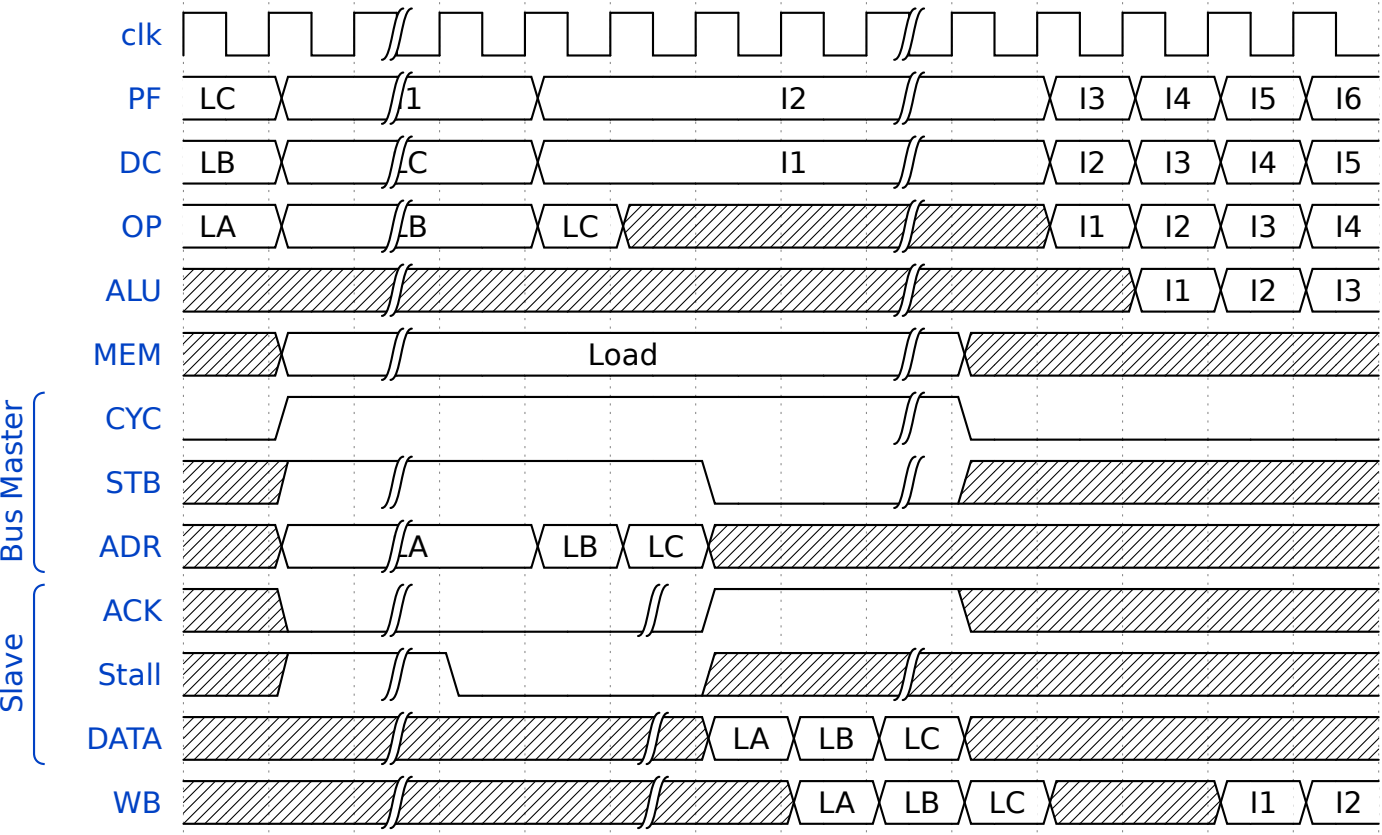
clk
PF
DC
OP
ALU
MEM
CYC
STB
ADR
ACK
Stall
DATA
WB
Bus Master
Slave
LC
1
I2
I3
I4
I5
I6
LB
C
I1
I2
I3
I4
I5
LA
B
LC
I1
I2
I3
I4
I1
I2
I3
Load
A
LB
LC
LA
LB
LC
LA
LB
LC
I1
I2