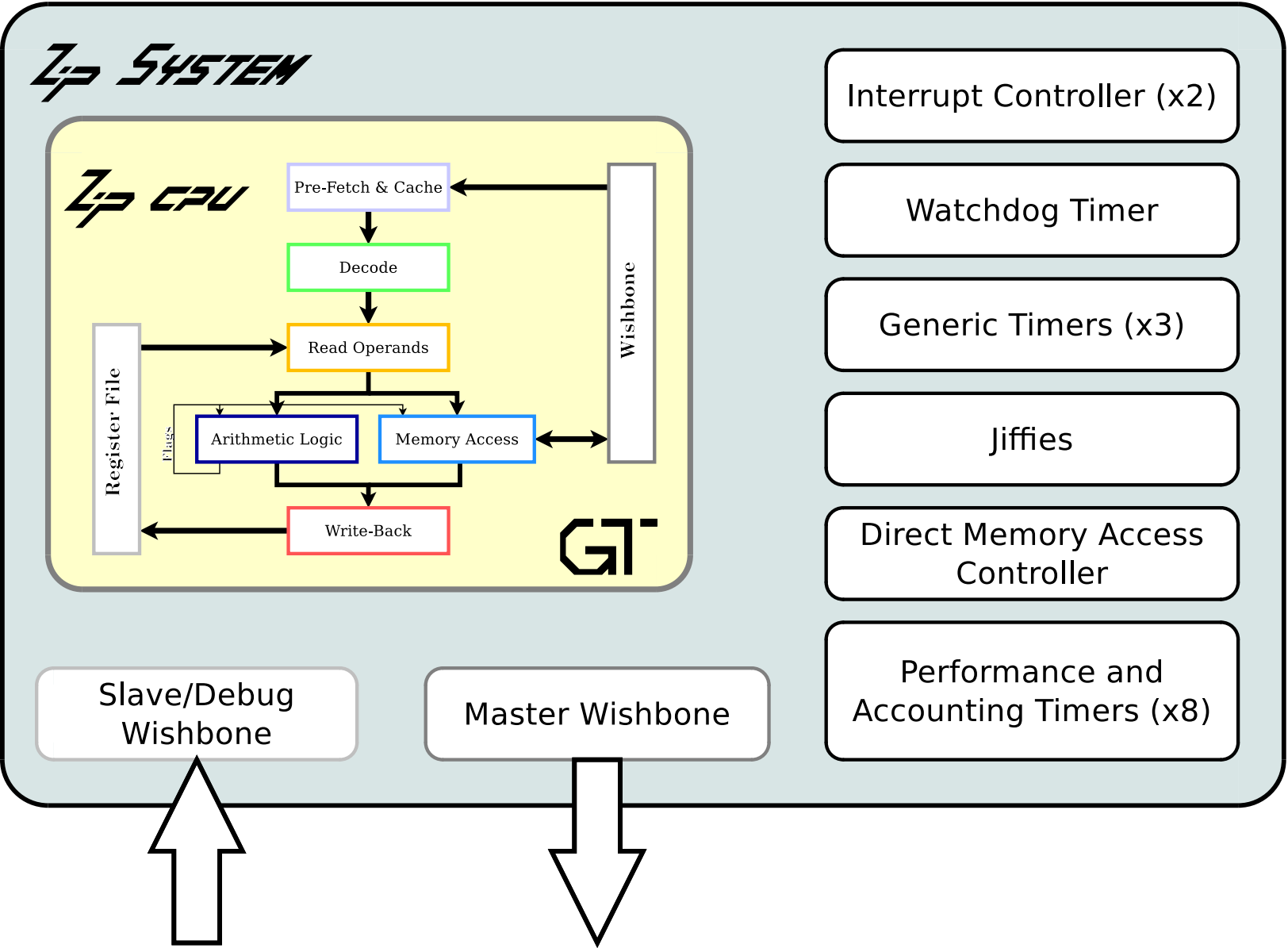
Zip System
Zip CPU
Pre-Fetch & Cache
Decode
Read Operands
Arithmetic Logic
Memory Access
Write-Back
Register File
Flags
Wishbone
Interrupt Controller (x2)
Watchdog Timer
Generic Timers (x3)
Jiffies
Direct Memory Access Controller
Performance and Accounting Timers (x8)
Slave/Debug Wishbone
Master Wishbone