nM1_out INPUT VCC — OUTPUT pin_nM1

pin_control_oe INPUT VCC

nMREQ_out INPUT VCC — TRI inst19 — OUTPUT pin_nMREQ

nIORQ_out INPUT VCC — TRI inst20 — OUTPUT pin_nIORQ

nRD_out INPUT VCC — TRI inst21 — OUTPUT pin_nRD

nWR_out INPUT VCC — TRI inst22 — OUTPUT pin_nWR

nRFSH_out INPUT VCC — OUTPUT pin_nRFSH

in_halt INPUT VCC — NOT inst26 — OUTPUT pin_nHALT

mwait OUTPUT — NOT inst27 — INPUT VCC pin_nWAIT

busrq OUTPUT — NOT inst25 — INPUT VCC pin_nBUSRQ

busack INPUT VCC — NOT inst28 — OUTPUT pin_nBUSACK

clk OUTPUT — INPUT VCC CPUCLK

intr OUTPUT — NOT inst29 — INPUT VCC pin_nINT

nmi OUTPUT — NOT inst30 — INPUT VCC pin_nNMI

reset_in OUTPUT — NOT inst31 — INPUT VCC pin_nRESET

PROJECT	**A-Z80**	MODULE	**control_pins_n**
DESIGNER	Goran Devic		
DATE	July 6, 2014	REV	1.1

control_pins_n.bdf