

		LSB																	
		0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F		
M S B	0	BRK Implied 1 7	ORA (IND,X) 2 6			TSB ZP 2 5	ORA ZP 2 3	ASL ZP 2 5	RMB0 ZP 2 5	PHP Implied 1 3	ORA IMM 2 2	ASL Accum 1 2		TSB ABS 3 6	ORA ABS 3 4	ASL ABS 3 6	BBR0 ZP 3 5**	0	
	1	BPL Relative 2 2**	ORA (IND),Y 2 5*	ORA (IND) 2 5		TRB ZP 2 5	ORA ZP,X 2 4	ASL ZP,X 2 6	RMB1 ZP 2 5	CLC Implied 1 3	ORA ABS,Y 3 4*	INC Accum 1 2		TRB ABS 3 6	ORA ABS,X 3 4*	ASL ABS,X 3 6*	BBR1 ZP 3 5**	1	
	2	JSR ABS 3 6	AND (IND,X) 2 6			BIT ZP 2 3	AND ZP 2 3	ROL ZP 2 5	RMB2 ZP 2 5	PLP Implied 1 4	AND IMM 2 2	ROL Accum 1 2		BIT ABS 3 4	AND ABS 3 4	ROL ABS 3 6	BBR2 ZP 3 5**	2	
	3	BMI Relative 2 2**	AND (IND),Y 2 5*	AND (IND) 2 5		BIT ZP,X 2 4	AND ZP,X 2 4	ROL ZP,X 2 6	RMB3 ZP 2 5	SEC Implied 1 3	AND ABS,Y 2 4*	DEC Accum 1 2		BIT ABS,X 3 4*	AND ABS,X 3 4*	ROL ABS,X 3 6*	BBR3 ZP 3 5**	3	
	4	RTI Implied 1 6	EOR (IND,X) 2 6				EOR ZP 2 3	LSR ZP 2 5	RMB4 ZP 2 5	PHA Implied 1 3	EOR IMM 2 2	LSR Accum 1 2		JMP ABS 3 3	EOR ABS 3 4	LSR ABS 3 6	BBR4 ZP 3 5**	4	
	5	BVC Relative 2 2**	EOR (IND),Y 2 5*	EOR (IND) 2 5			EOR ZP,X 2 4	LSR ZP,X 2 6	RMB5 ZP 2 5	CLI Implied 1 3	EOR ABS,Y 3 4*	PHY Implied 1 3			EOR ABS,X 3 4*	LSR ABS,X 3 6*	BBR5 ZP 3 5**	5	
	6	RTS Implied 1 6	ADC (IND,X) 2 6			STZ ZP 2 3	ADC ZP 2 3+	ROR ZP 2 5	RMB6 ZP 2 5	PLA Implied 1 4	ADC IMM 2 2+	ROR Accum 1 2		JMP (IND) 3 6	ADC ABS 3 4+	ROR ABS 3 6	BBR6 ZP 3 5**	6	
	7	BVS Relative 2 2**	ADC (IND),Y 2 5*+	ADC (IND) 2 5		STZ ZP,X 2 4	ADC ZP,X 2 4+	ROR ZP,X 2 6	RMB7 ZP 2 5	SEI Implied 1 3	ADC ABS,Y 3 4*+	PLY Implied 1 4		JMP (IND,X) 3 6	ADC ABS,X 3 4*+	ROR ABS,X 3 6*	BBR7 ZP 3 5**	7	
	8	BRA Relative 2 3*	STA (IND,X) 2 6			STY ZP 2 3	STA ZP 2 3	STX ZP 2 3	SMB0 ZP 2 5	DEY Implied 1 3	BIT IMM 2 2	TXA Implied 1 2			STY ABS 3 4	STA ABS 3 4	STX ABS 3 4	BBS0 ZP 3 5**	8
	9	BCC Relative 2 2**	STA (IND),Y 2 6*	STA (IND) 2 5		STY ZP,X 2 4	STA ZP,X 2 4	STX ZP,Y 2 4	SMB1 ZP 2 5	TYA Implied 1 3	STA ABS,Y 3 5*	TXS Implied 1 2			STZ ABS 3 4	STA ABS,X 3 5*	STZ ABS,X 3 5*	BBS1 ZP 3 5**	9
	A	LDY IMM 2 2	LDA (IND,X) 2 6	LDX IMM 2 2		LDY ZP 2 3	LDA ZP 2 3	LDX ZP 2 3	SMB2 ZP 2 5	TAY Implied 1 3	LDA IMM 2 2	TAX Implied 1 2			LDY ABS 3 4	LDA ABS 3 4	LDX ABS 3 4	BBS2 ZP 3 5**	A
	B	BCS Relative 2 2**	LDA (IND),Y 2 5*	LDA (IND) 2 5		LDY ZP,X 2 4	LDA ZP,X 2 4	LDX ZP,Y 2 4	SMB3 ZP 2 5	CLV Implied 1 3	LDA ABS,Y 3 4*	TSX Implied 1 2			LDY ABS,X 3 4*	LDA ABS,X 3 4*	LDX ABS,Y 3 4*	BBS3 ZP 3 5**	B
	C	CPY IMM 2 2	CMP (IND,X) 2 6			CPY ZP 2 3	CMP ZP 2 3	DEC ZP 2 5	SMB4 ZP 2 5	INY Implied 1 3	CMP IMM 2 2	DEX Implied 1 2			CPY ABS 3 4	CMP ABS 3 4	DEC ABS 3 6	BBS4 ZP 3 5**	C
	D	BNE Relative 2 2**	CMP (IND),Y 2 5*	CMP (IND) 2 5			CMP ZP,X 2 4	DEC ZP,X 2 6	SMB5 ZP 2 5	CLD Implied 1 3	CMP ABS,Y 3 4*	PHX Implied 1 3				CMP ABS,X 3 4*	DEC ABS,X 3 6*	BBS5 ZP 3 5**	D
	E	CPX IMM 2 2	SBC (IND,X) 2 6+			CPX ZP 2 3	SBC ZP 2 3+	INC ZP 2 5	SMB6 ZP 2 5	INX Implied 1 3	SBC IMM 2 2+	NOP Implied 1 2			CPX ABX 3 4	SBC ABS 3 4+	INC ABS 3 6	BBS6 ZP 3 5**	E
	F	BEQ Relative 2 2**	SBC (IND),Y 2 5*+	SBC (IND) 2 5			SBC ZP,X 2 4+	INC ZP,X 2 6	SMB7 ZP 2 5	SED Implied 1 3	SBC ABS,Y 3 4*+	PLX Implied 1 4				SBC ABS,X 3 4*+	INC ABS,X 3 6*	BBS7 ZP 3 5**	F
		0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F		

+ Add 1 to N if in decimal mode, * ADD 1 to N if page boundary is crossed

** Add 1 to N if branch occurs to same page or add 2 to N if branch occurs to different page