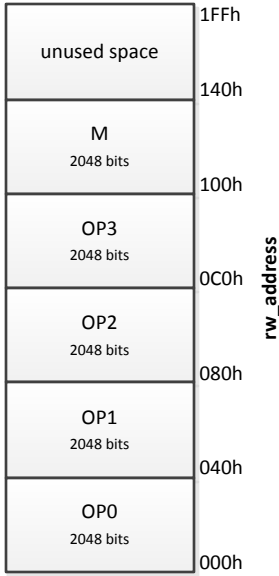
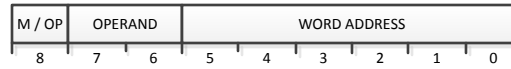
mod_sim_exp_core
memory mapping
(Word adressing)





address structure:
bit: 8 -> '1': modulus
'0': operands
bits: 7-6 -> operand select in case of bit 8 = '0'
don't care in case of modulus
bits: 5-0 -> modulus addr / operand addr resp.